

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for LED Lighting and switching mode power supplies.

FEATURES

- $V_{DSS(Min.)} = 400V$, $I_D = 2A$
- Drain-Source ON Resistance : $R_{DS(ON)} = 3.4$ (max) @ $V_{GS} = 10V$
- $Q_g(\text{typ.}) = 4.4nC$

ORDERING INFORMATION

FULL NAME	PACKAGE	SUFFIX
KF3N40W-RTK/HSD	SOT-223	-
KF3N40W-RTK/HSDH	SOT-223(1)	Added code "H" to SOT-223(1)

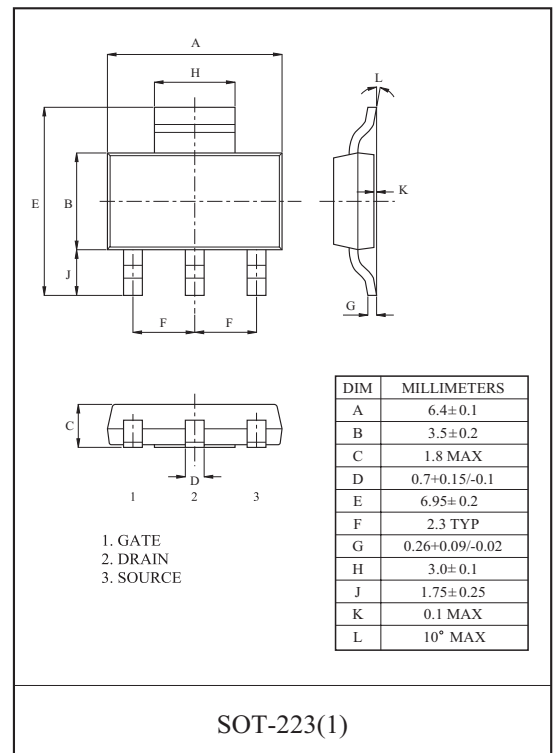
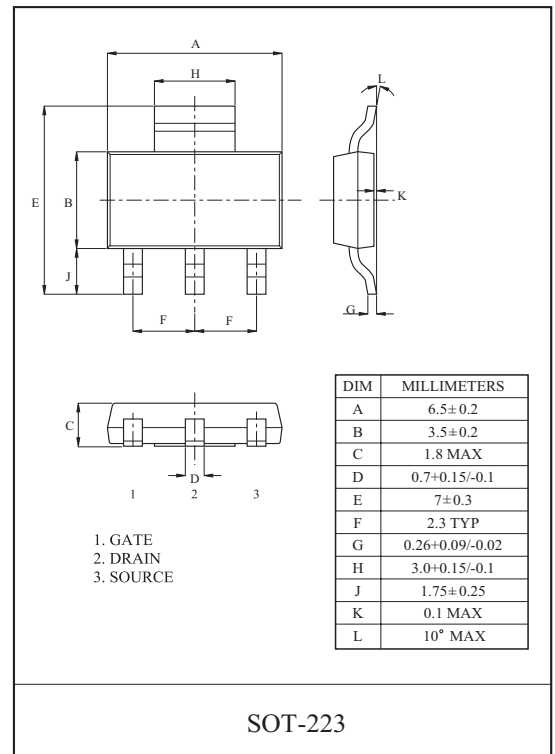
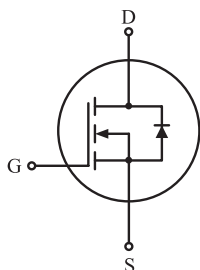
MAXIMUM RATING (Tc=25)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V_{DSS}	400	V
Gate-Source Voltage	V_{GSS}	± 30	V
Drain Current	@ $T_C=25$	2*	A
	@ $T_C=100$	1.2*	
	Pulsed (Note1)	6*	
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	52	mJ
Repetitive Avalanche Energy (Note 1)	E_{AR}	0.2	mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Drain Power Dissipation	$T_A=25$	2**	W
	Derate above 25	0.02	W/
Maximum Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	
Thermal Characteristics			
Thermal Resistance, Junction-to-Case	R_{thJC}	10.0	/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5**	/W

* : Drain current limited by maximum junction temperature.

** : Surface Mounted on FR4 Board 2 Inch x 2 Inch x 1mm.

PIN CONNECTION



KF3N40W

ELECTRICAL CHARACTERISTICS (Tc=25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\ \mu A, V_{GS}=0V$	400	-	-	V
Breakdown Voltage Temperature Coefficient	BV_{DSS}/T_j	$I_D=250\ \mu A$, Referenced to 25	-	0.4	-	V/
Drain Cut-off Current	I_{DSS}	$V_{DS}=400V, V_{GS}=0V$,	-	-	10	μA
Gate Threshold Voltage	V_{th}	$V_{DS}=V_{GS}, I_D=250\ \mu A$	2.5	-	4.5	V
Gate Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	± 100	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=1A$	-	2.8	3.4	
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=320V, I_D=2.2A$ $V_{GS}=10V$ (Note4,5)	-	4.4	5.8	nC
Gate-Source Charge	Q_{gs}		-	1.0	-	
Gate-Drain Charge	Q_{gd}		-	2.0	-	
Turn-on Delay time	$t_{d(on)}$	$V_{DD}=200V, I_D=2.2A$ $R_G=25$ (Note4,5) $V_{GS}=10V$	-	10	-	ns
Turn-on Rise time	t_r		-	11	-	
Turn-off Delay time	$t_{d(off)}$		-	20	-	
Turn-off Fall time	t_f		-	17	-	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$	-	163	211	pF
Output Capacitance	C_{oss}		-	26	-	
Reverse Transfer Capacitance	C_{rss}		-	2.5	-	
Source-Drain Diode Ratings						
Continuous Source Current	I_S	$V_{GS}<V_{th}$	-	-	2	A
Pulsed Source Current	I_{SP}		-	-	8	
Diode Forward Voltage	V_{SD}	$I_S=2A, V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time	t_{rr}	$I_S=2.2A, V_{GS}=0V$, $dI_S/dt=100A/\mu s$	-	240	-	ns
Reverse Recovery Charge	Q_{rr}		-	0.65	-	μC

Note 1) Repetivity rating : Pulse width limited by junction temperature.

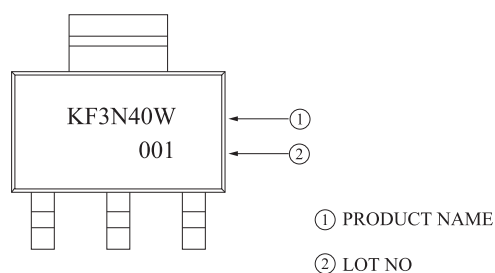
Note 2) $L = 23mH, I_S=2A, V_{DD}=50V, R_G = 25$, Starting $T_j = 25$.

Note 3) $I_S = 2A, dI/dt = 200A/\mu s, V_{DD} = BV_{DSS}$, Starting $T_j = 25$.

Note 4) Pulse Test : Pulse width $300\mu s$, Duty Cycle 2% .

Note 5) Essentially independent of operating temperature.

Marking



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Fig1. $I_D - V_{DS}$

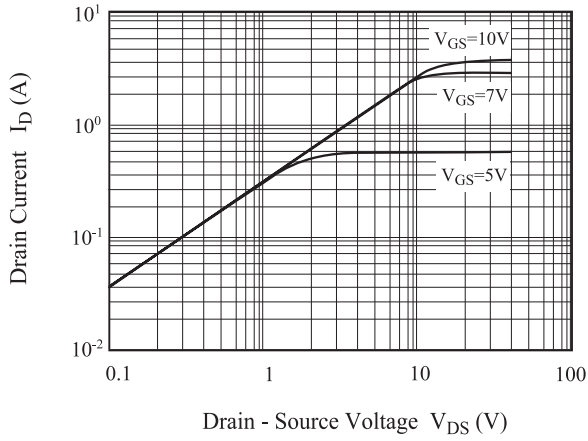


Fig2. $I_D - V_{GS}$

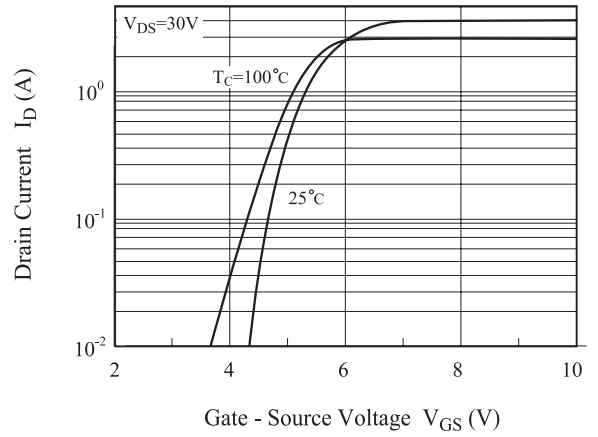


Fig3. $BV_{DSS} - T_j$

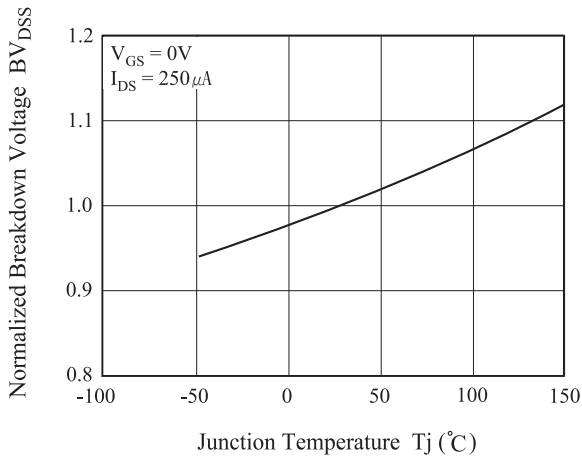


Fig4. $R_{DS(ON)} - I_D$

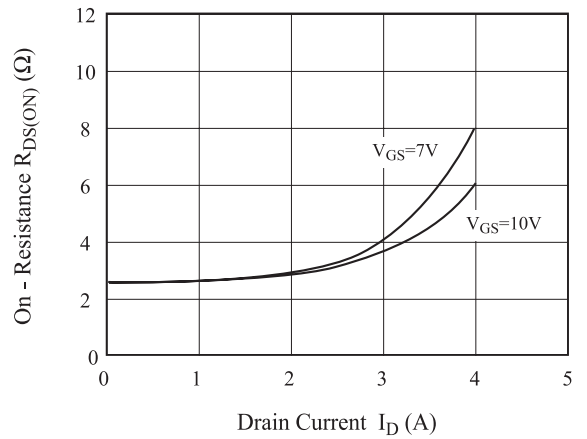


Fig5. $I_S - V_{SD}$

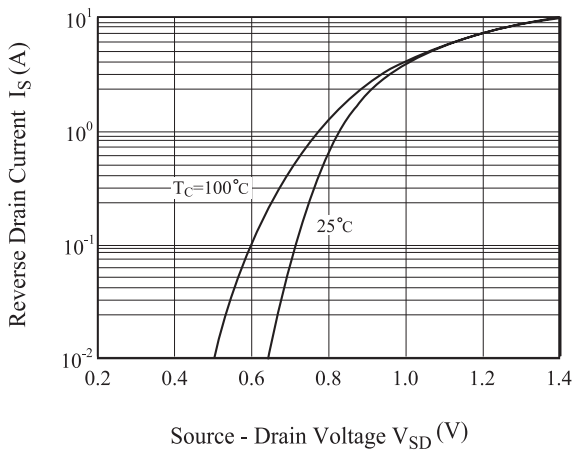
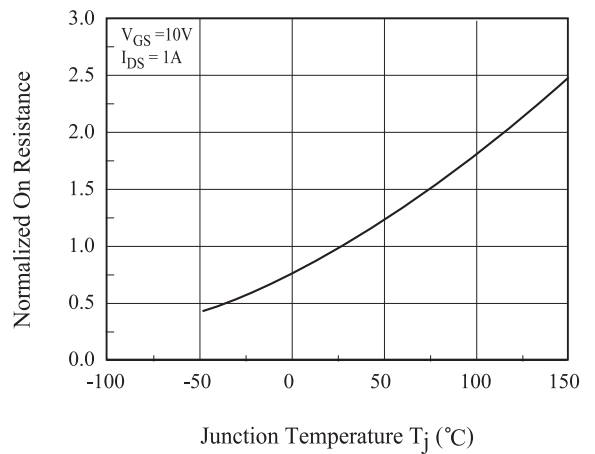


Fig6. $R_{DS(ON)} - T_j$



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Fig 7. C - V_{DS}

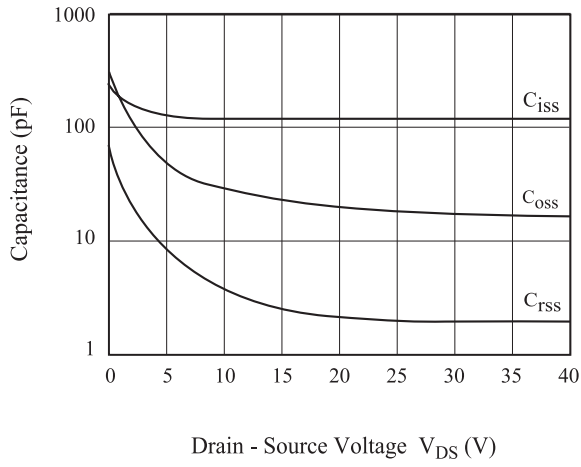


Fig8. Q_g - V_{GS}

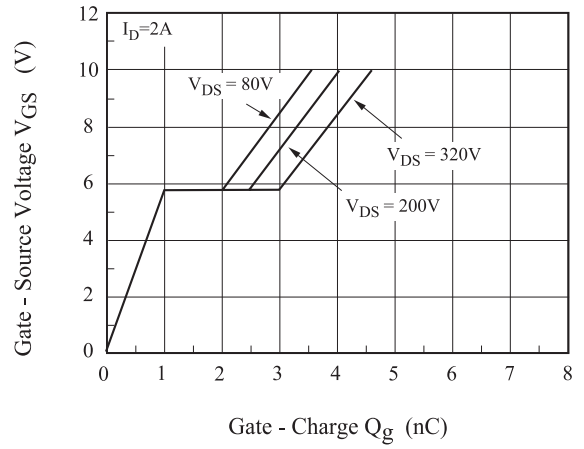


Fig9. Safe Operation Area

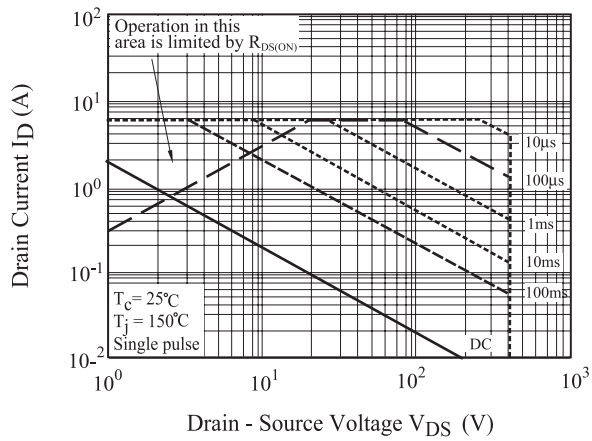


Fig10. I_D - T_j

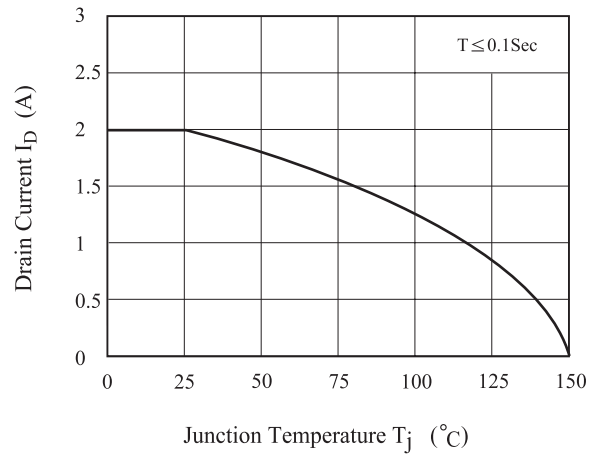
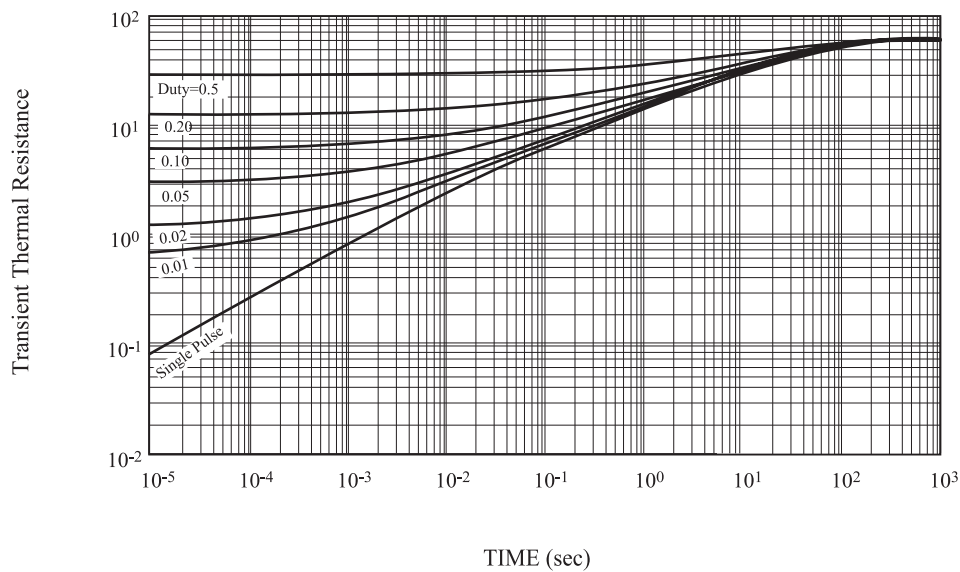


Fig11. Transient Thermal Response Curve



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Fig12. Gate Charge

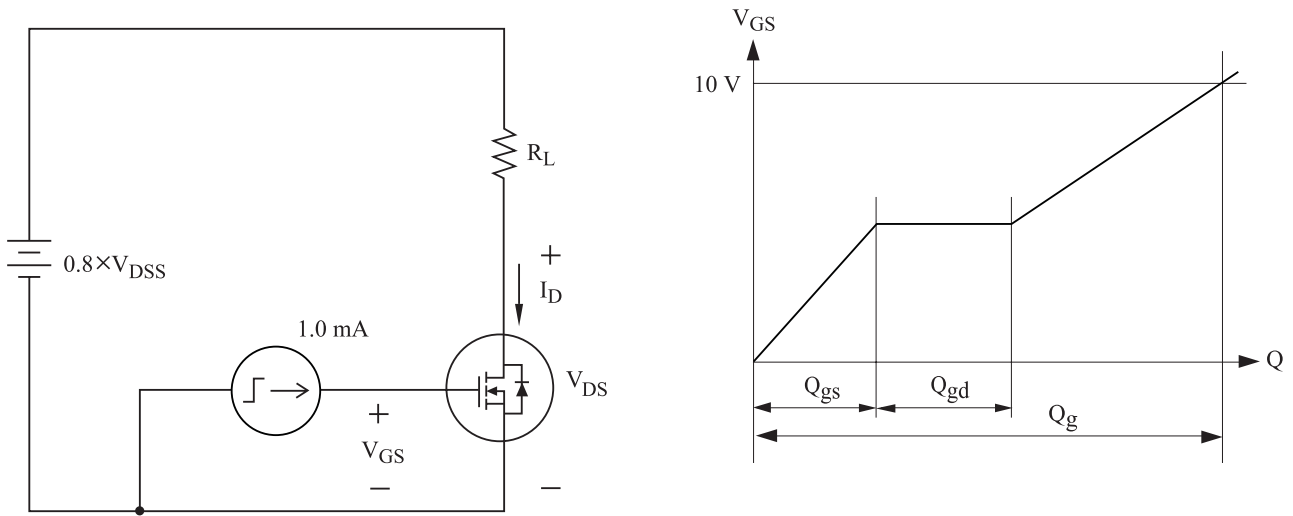


Fig13. Single Pulsed Avalanche Energy

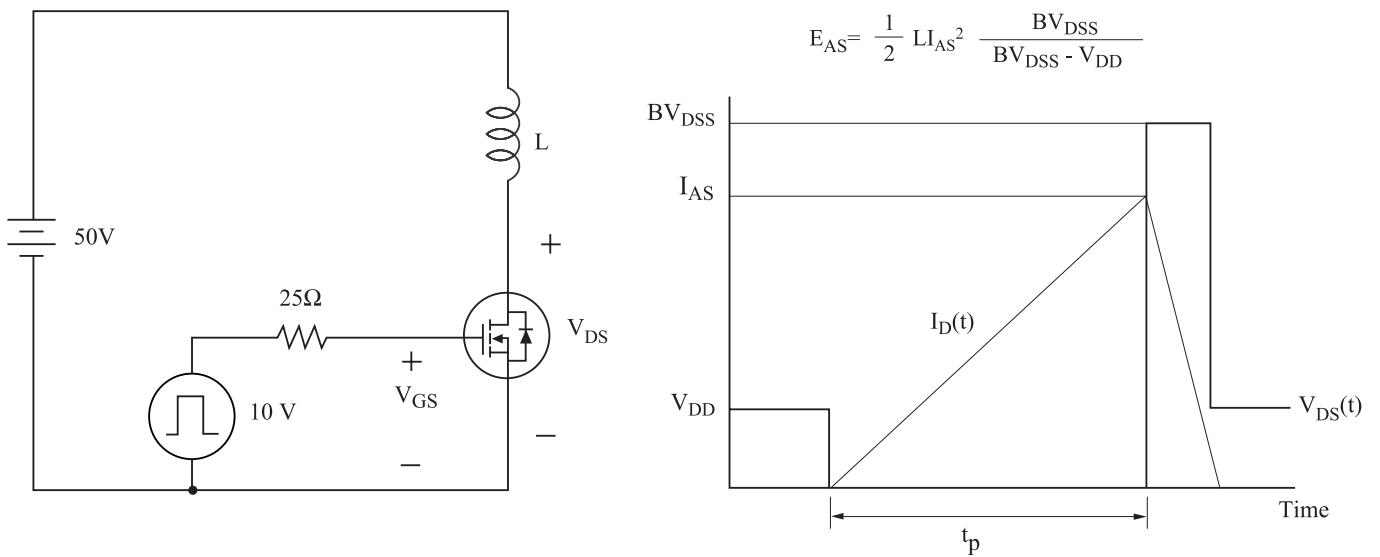


Fig14. Resistive Load Switching

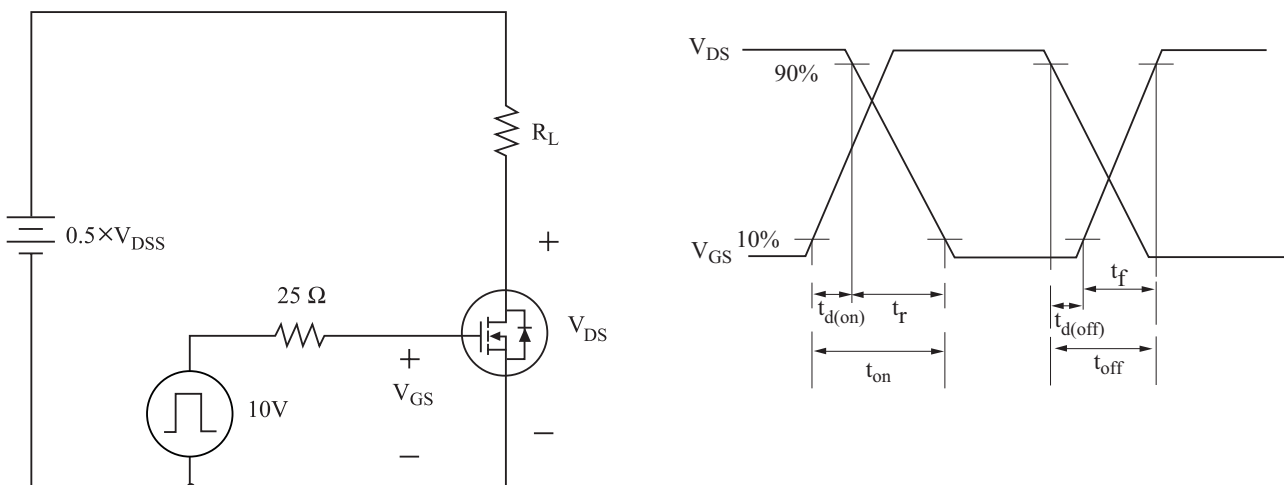
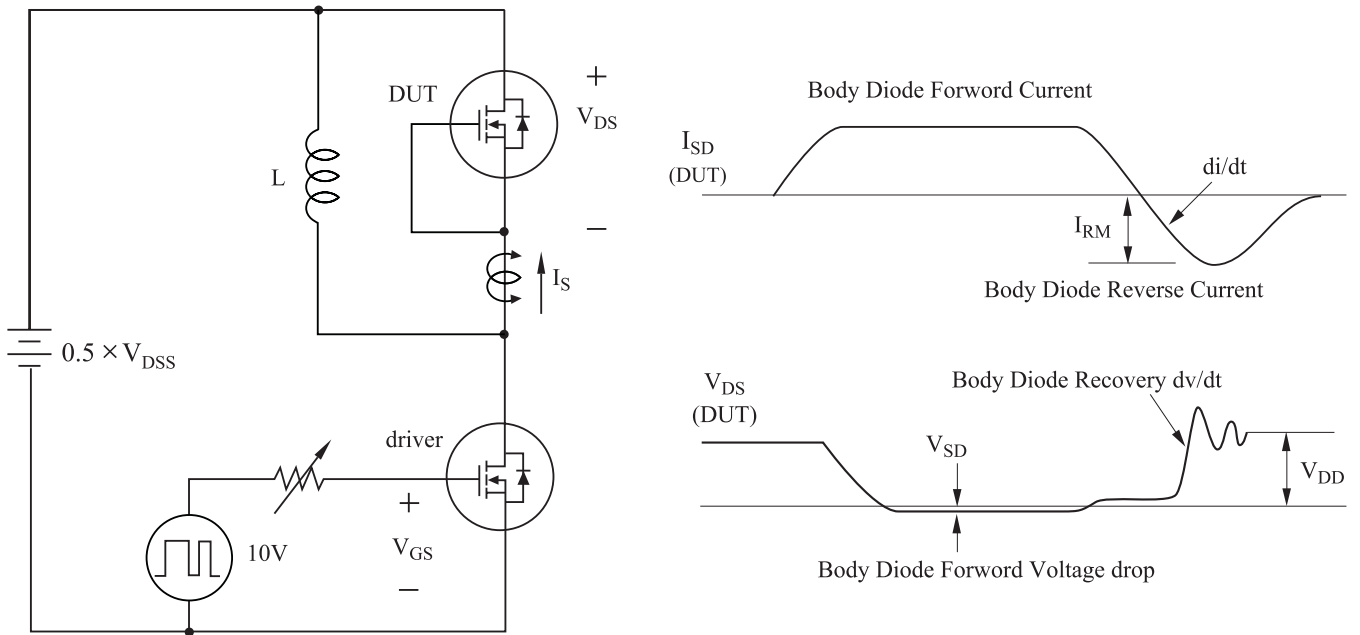


Fig15. Source - Drain Diode Reverse Recovery and dv/dt



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Fig16. Recommended Pad Dimension

