

N-Ch Super Junction MOSFET

KP280N60FWM3Z

This Super Junction MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge, low EMI characteristics and excellent avalanche characteristics. It is mainly suitable for power factor correction and switching mode power supplies.

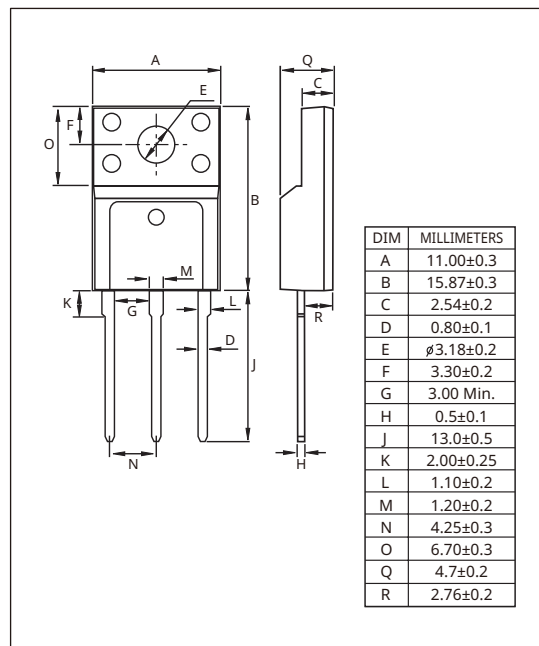
FEATURES

- $V_{DSS}=600V$, $I_D=15A$
- Drain-Source ON Resistance:
 $R_{DS(ON)}(MAX)=280m\Omega$ @ $V_{GS}=10V$
- $Q_g(typ.)=21nC$ @ $V_{GS}=10V$
- Excellent ESD robustness > 2kV(HBM)

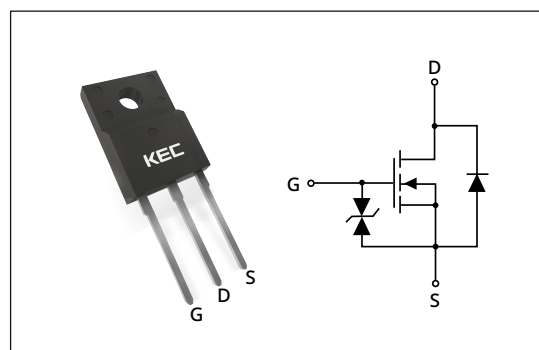
APPLICATION

- PFC Stages
- Resonant switching stages
- Fly Back converters
- Power of PC, TV, Lighting, Adapter, Server and Telecom

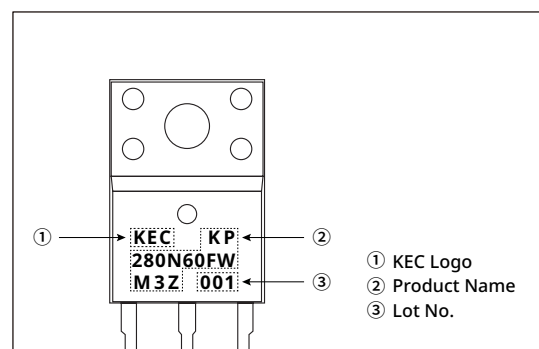
PACKAGE DIMENSION(TO-220ISW)



PIN CONFIGURATION



MARKING CODE



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N-Ch Super Junction MOSFET - KP280N60FWM3Z

MAXIMUM RATING (T_J=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V _{DSS}	600	V
Gate-Source Voltage		V _{GSS}	±25	V
Drain Current	@T _C =25°C	I _D	15*	A
	@T _C =100°C		9.5*	
	Pulsed (Note 1)	I _{DP}	45*	
Single Pulsed Avalanche Energy (Note 2)		E _{AS}	251	mJ
Repetitive Avalanche Energy (Note 1)		E _{AR}	5.3	mJ
Peak MOSFET dv/dt		dv/dt	50	V/ns
Peak Diode Recovery dv/dt (Note 3)		dv/dt	15	V/ns
Drain Power Dissipation	T _C =25°C	P _D	50	W
	T _A =25°C		2	W
Maximum Junction Temperature		T _J	150	°C
Operation and Storage Temperature Range		T _{opr} , T _{stg}	-55 ~ 150	°C
Thermal Characteristics				
Thermal Resistance, Junction-to-Case		R _{thJC}	2.5	°C/W
Thermal Resistance, Junction-to-Ambient		R _{thJA}	62.5	°C/W

* : Drain current limited by maximum junction temperature

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ELECTRICAL CHARACTERISTICS(T_j=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	600	-	-	V
Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} /ΔT _j	I _D =1mA, Referenced to 25°C	-	0.7	-	V/°C
Drain Cut-off Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	1	μA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Gate Leakage Current	I _{GSS}	V _{GS} =±25V, V _{DS} =0V	-	-	±10	μA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =7.5A	-	245	280	mΩ
Dynamic						
Total Gate Charge	Q _g	V _{DD} =400V, I _D =7.5A, V _{GS} =10V (Note 4)	-	23	-	nC
Gate-Source Charge	Q _{gs}		-	5	-	
Gate-Drain Charge	Q _{gd}		-	8	-	
Turn-on Delay time	t _{d(on)}	V _{DD} =400V, I _D =7.5A, V _{GS} =10V, R _G =25Ω (Note 4)	-	35	-	ns
Turn-on Rise time	t _r		-	25	-	
Turn-off Delay time	t _{d(off)}		-	120	-	
Turn-off Fall time	t _f		-	22	-	
Input Capacitance	C _{iss}	V _{DS} =400V, V _{GS} =0V, f=1.0MHz	-	990	-	pF
Output Capacitance	C _{oss}		-	25	-	
Reverse Transfer Capacitance	C _{rss}	V _{DS} =400V, V _{GS} =0V, f=250kHz	-	1	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} =0V, V _{DS} =0...400V (Note 5)	-	45	-	
Effective output capacitance, time related	C _{o(tr)}	V _{GS} =0V, V _{DS} =0...400V (Note 6)	-	230	-	
Gate Resistance	R _G	V _{GS} =0V, f=1MHz	-	19	-	Ω
Source-Drain Diode Ratings						
Continuous Source Current	I _S	V _{GS} < V _{th} , T _C =25°C	-	-	15	A
Pulsed Source Current	I _{SP}		-	-	45	
Diode Forward Voltage	V _{SD}	I _S =7.5A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	I _S =7.5A, V _{GS} =0V, V _{DD} =400V dI _S /dt=100A/μs	-	250	-	ns
Reverse Recovery Charge	Q _{rr}		-	2.7	-	μC
Reverse Recovery Current	I _{rrm}		-	22	-	A

Note 1) Repetivity rating: Pulse width limited by junction temperature.

Note 2) L=33.6 mH, I_D=3.75A, V_{DD}=50V, R_G=25Ω, Starting T_j=25°C.

Note 3) I_S ≤ I_D, di/dt ≤ 100A/μs, V_{DD} ≤ BV_{DSS}, Starting T_j=25°C.

Note 4) Essentially independent of operating temperature.

Note 5) C_{o(er)} is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V.

Note 6) C_{o(tr)} is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 400V.

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Fig 1. $I_D - V_{DS}$ - 1

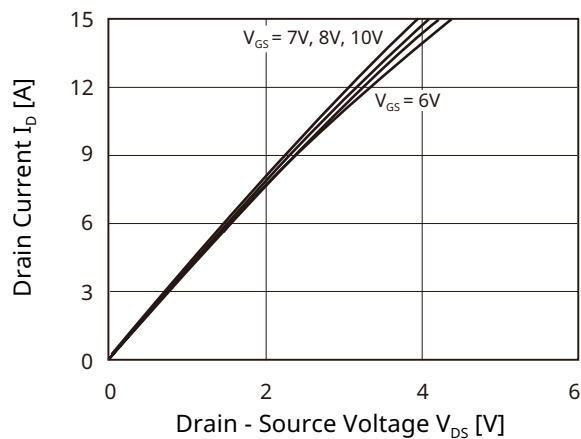


Fig 2. $I_D - V_{DS}$ - 2

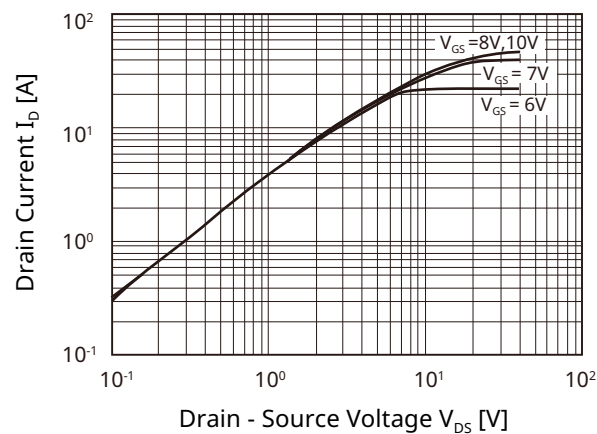


Fig 3. $I_D - V_{GS}$

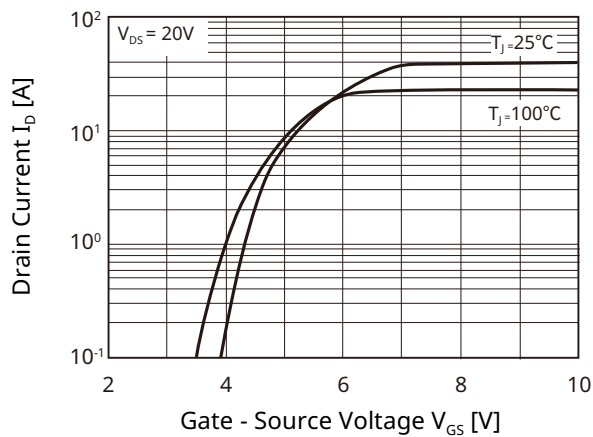


Fig 4. $R_{DS(ON)} - I_D$

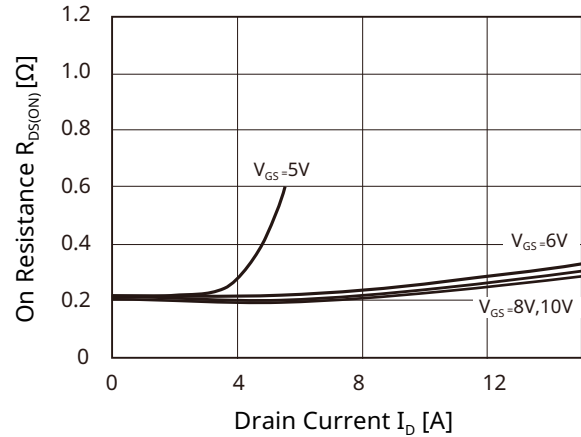


Fig 5. $R_{DS(ON)} - V_{GS}$

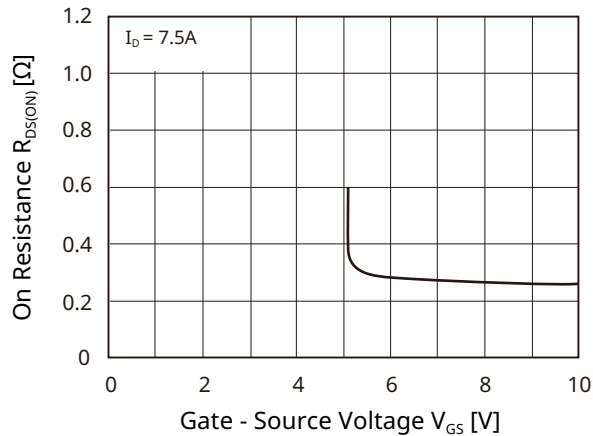
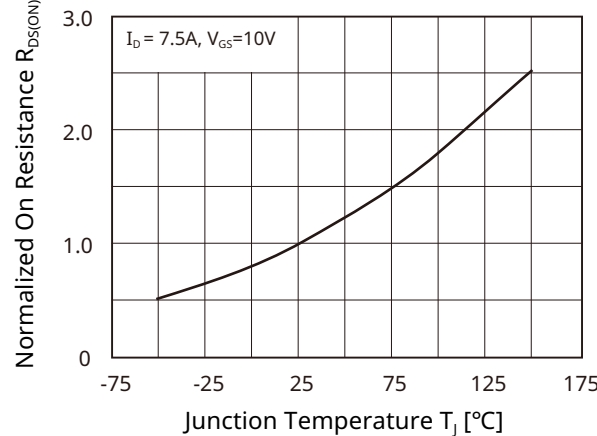


Fig 6. $R_{DS(ON)} - T_J$



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Fig 7. $BV_{DSS} - T_J$

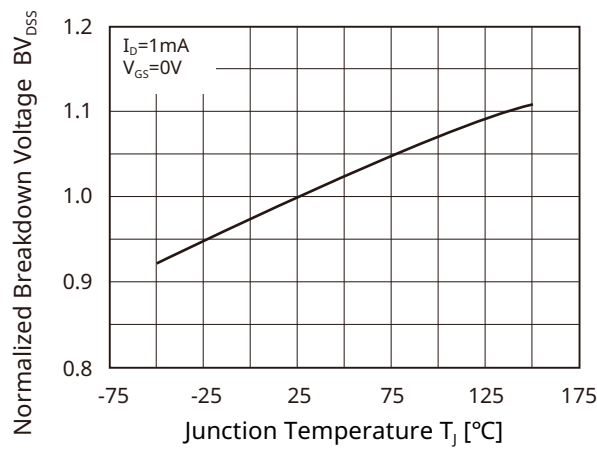


Fig 8. $V_{th} - T_J$

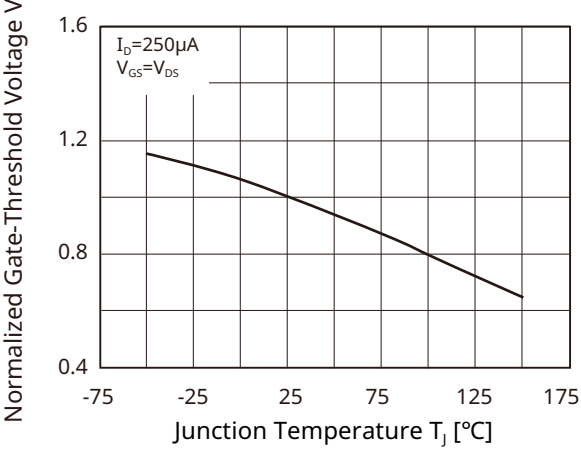


Fig 9. $I_S - V_{SD}$

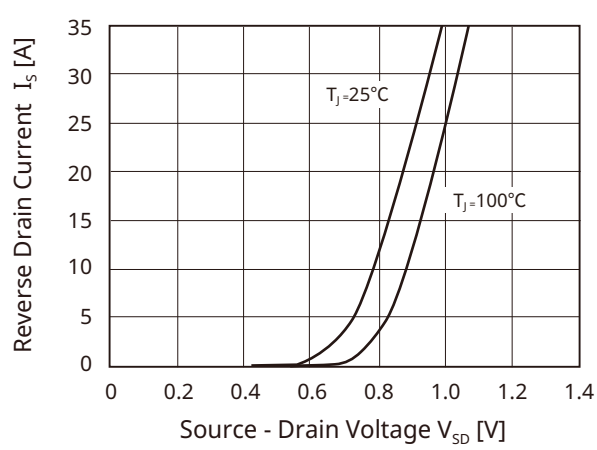


Fig10. Capacitance V_{DS}

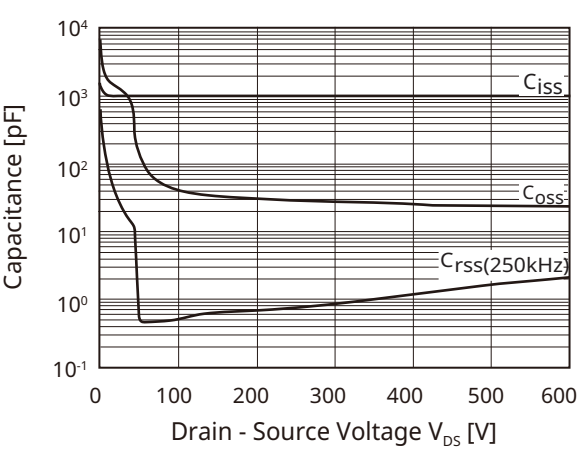


Fig 11. $E_{OSS} - V_{DS}$

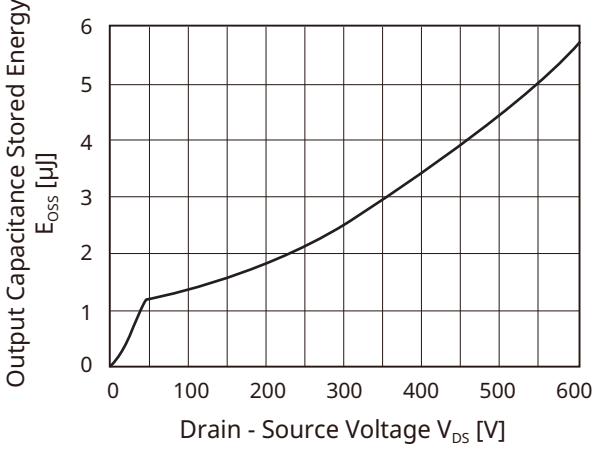
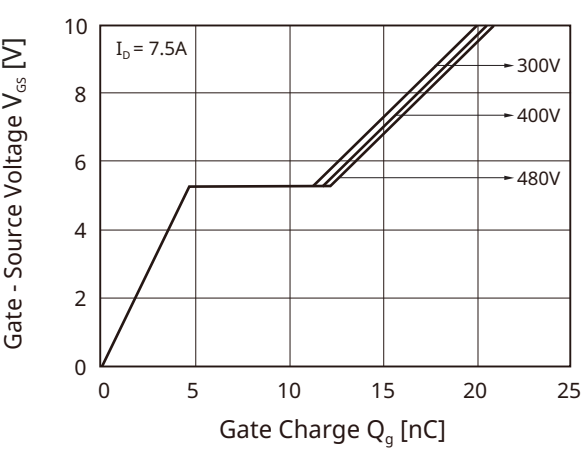


Fig 12. $Q_g - V_{GS}$



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Fig 13. Ptot - T_C

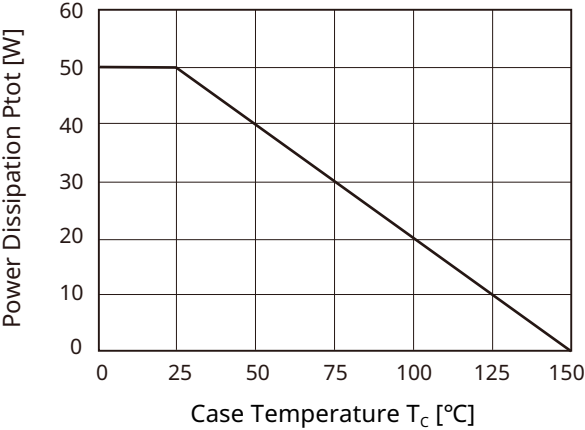


Fig 14. Switching Time - I_D

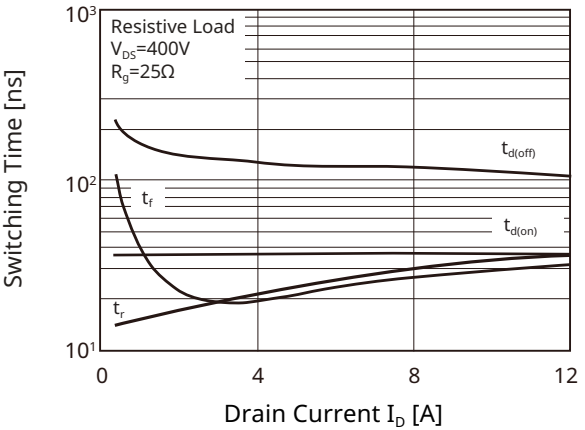


Fig 15. Switching Loss - I_D

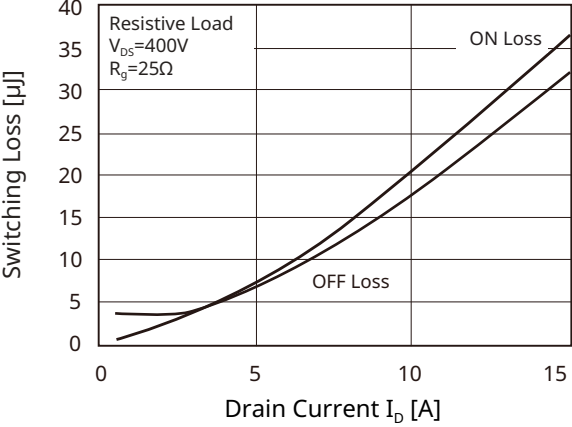


Fig 16. Switching Time - R_g

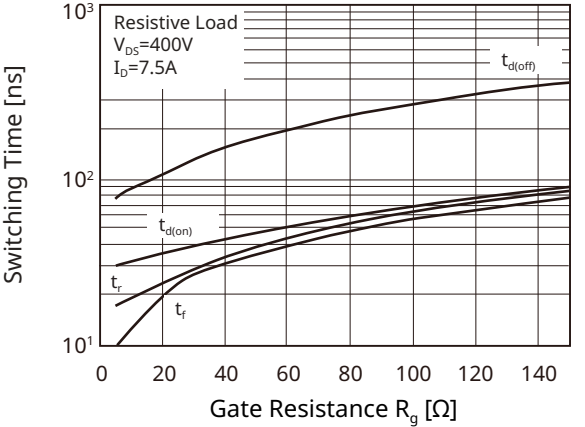


Fig 17. Switching Loss - R_g

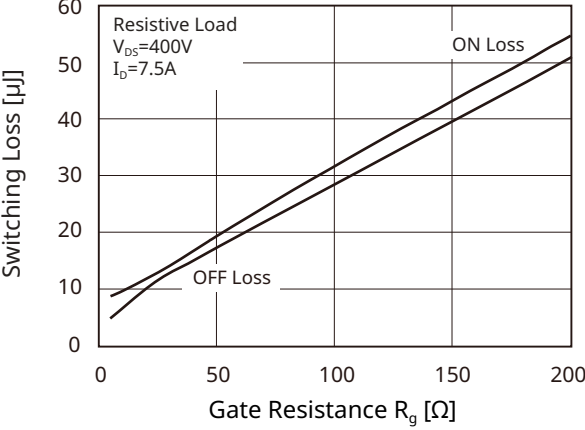
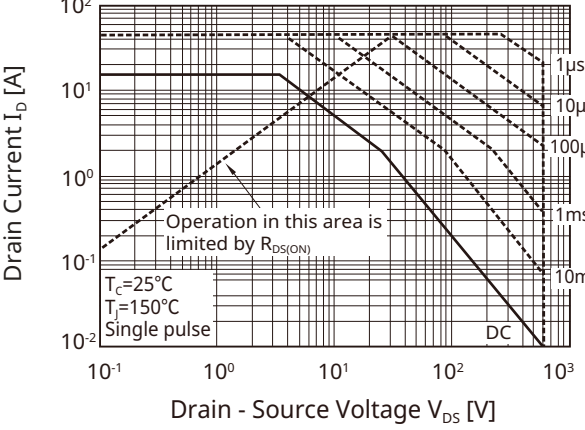


Fig 18. Safe Operation Area



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Fig 19. Transient Rth Curve (J-C)

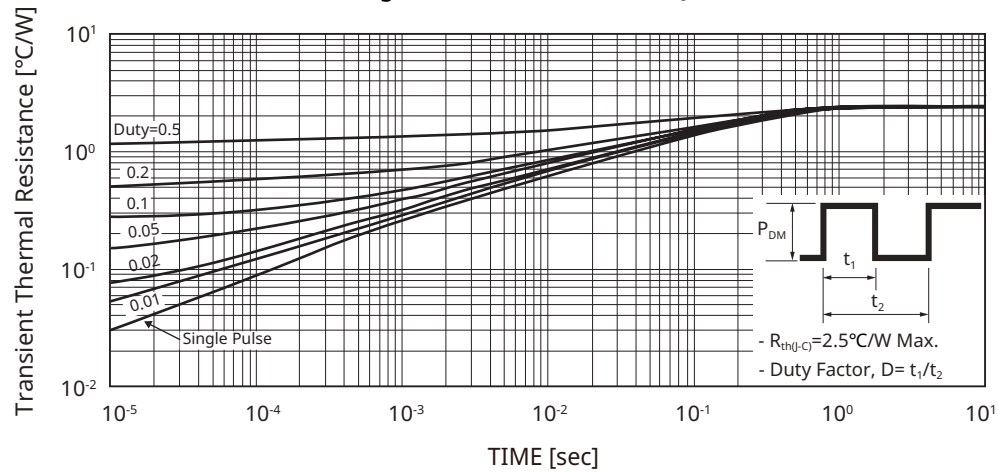


Fig20. Gate Charge

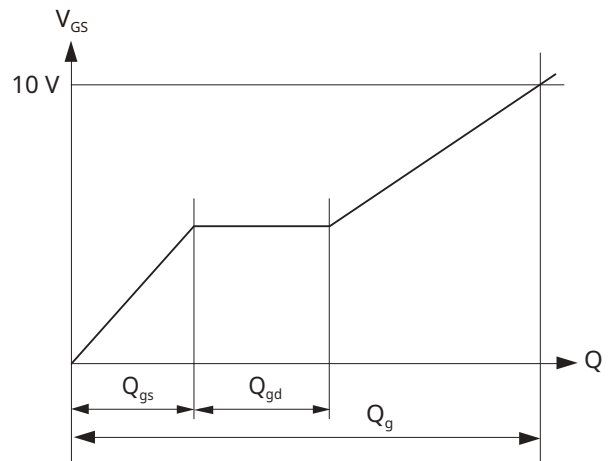
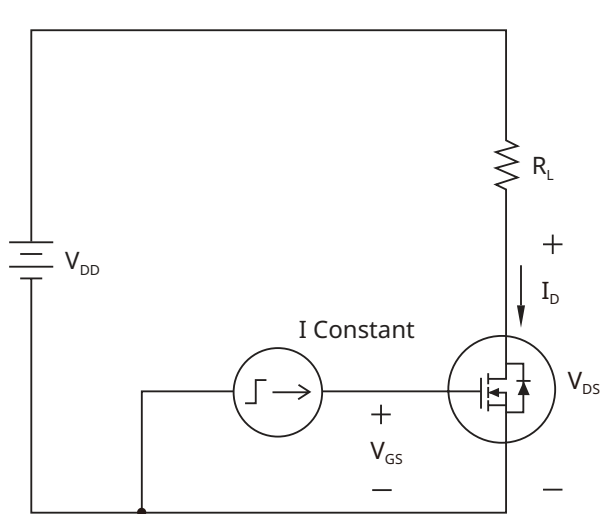
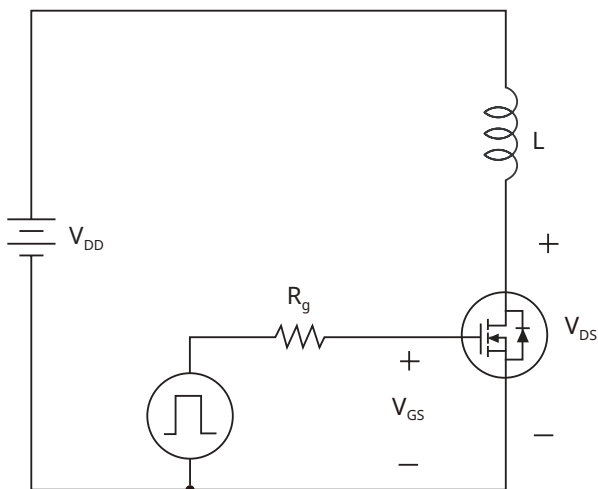


Fig21. Single Pulsed Avalanche Energy



$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DS}}{BV_{DS} - V_{DD}}$$

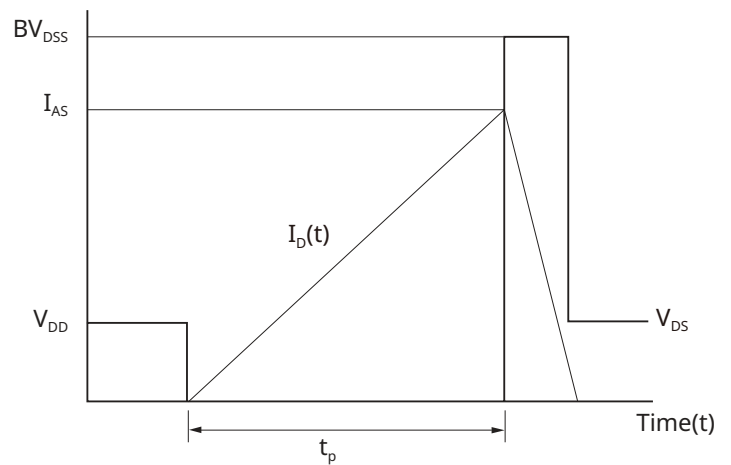
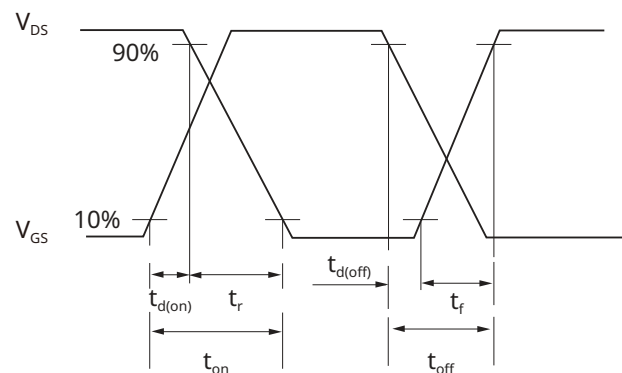
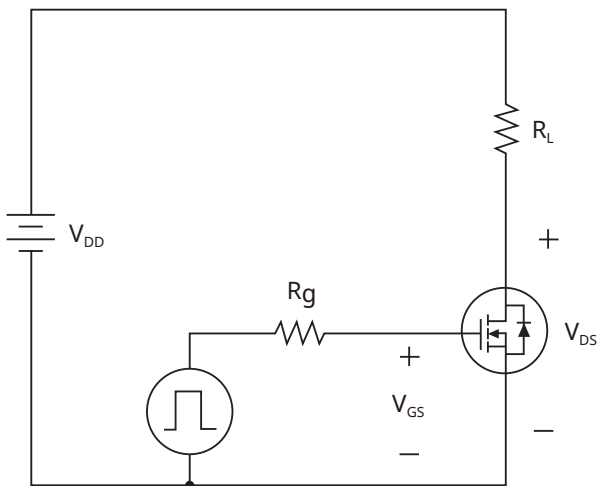


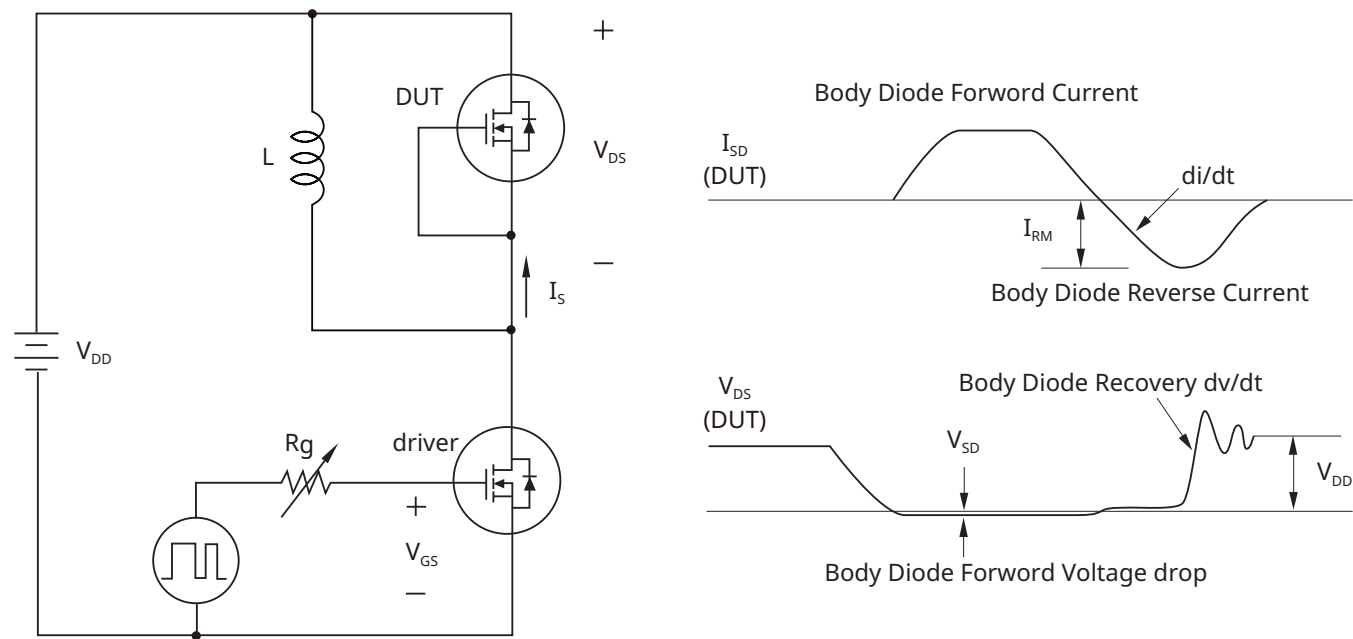
Fig22. Resistive Load Switching



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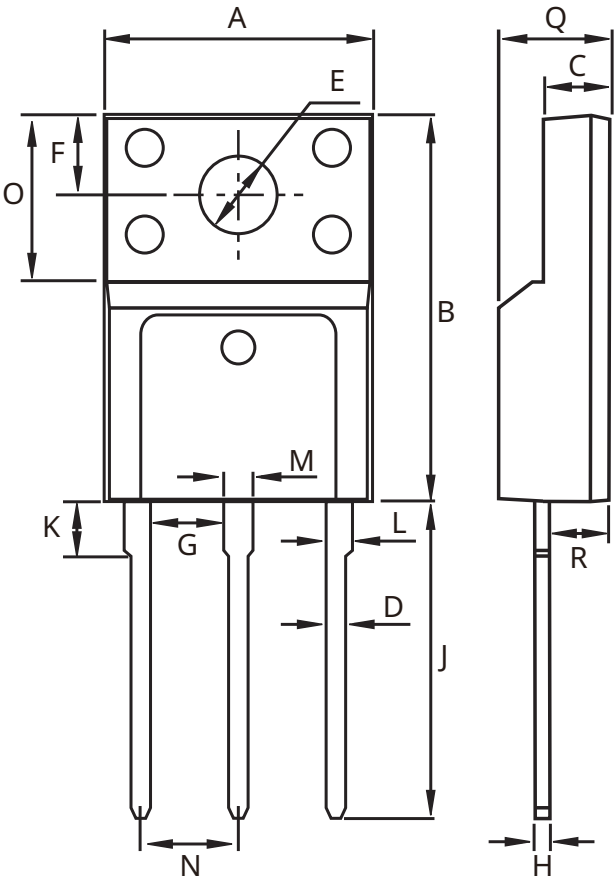
Fig23. Source - Drain Diode Reverse Recovery and dv/dt



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PACKAGE INFORMATION(TO-220ISW)



DIM	MILLIMETERS
A	11.00±0.3
B	15.87±0.3
C	2.54±0.2
D	0.80±0.1
E	ø3.18±0.2
F	3.30±0.2
G	3.00 Min.
H	0.5±0.1
J	13.0±0.5
K	2.00±0.25
L	1.10±0.2
M	1.20±0.2
N	4.25±0.3
O	6.70±0.3
Q	4.7±0.2
R	2.76±0.2

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ORDERING INFORMATION

Part Number	Status	Compliance		Packing Type	Reel Size	Quantity
		AEC-Q	Halogen Free			
KP280N60FWM3Z-U/H	Active	-	-	Tube	-	45

PRECAUTION ON USING KEC PRODUCTS

1. The products described in this data are intended to be used in general-purpose electronic equipment.
(Office equipment, telecommunication equipment, measuring equipment, home appliances)
2. When you intend to use these products with equipment or device which require an extremely high of reliability and special applications (such as automobile, air travel aerospace, transportation equipment, life support, system and safety devices) in which special quality and reliability and the failure or malfunction of products may directly jeopardize or harm the human body or damage to property and any application other than the standard application intended, please be sure to consult with our sales representative in advance.
3. On designing your application, please use product within the ranges guaranteed by KEC for maximum rating, operating supply voltage range, heat radiation characteristics and other characteristics. User shall be responsible for failure or damage when used beyond the guaranteed ranges.
4. The technical information described in this data is limited to showing representative characteristics and applied circuit examples of the products and it does not constitute the warranting of industrial property, the granting of relative rights, or the granting of any license.
5. What are described in the data may be changed without any prior notice to reflect new technical development. Please confirm that you have received the latest product standards or specification before final design, purchase or use.
6. Although KEC is continuously working to improve product reliability and quality, semiconductors can break down and malfunction due to various factors. Therefore, in order to prevent personal injury or fire arising from failure, please take safety measures such as complying with the derating characteristics, implementing redundant and fire prevention designs, and utilizing backups and fail-safe procedures. KEC shall have no responsibility for any damages arising out of the use of our Products beyond the rating specified by KEC.

For additional information,
please contact your local Sales Representative.

SOUTH KOREA (Headquarters)
KEC CORPORATION
PHONE : +82-2-2025-5000
sales@kec.co.kr

JAPAN
KEC JAPAN CO.,LTD.
PHONE : +81-03-5475-2691
kec_jp@kec.co.kr

SINGAPORE
KEC SINGAPORE PTE., LTD.
PHONE : +65-6748-7372(#102)
kec_sg@kec.co.kr

THAILAND
KEC THAILAND BANGKOK
OFFICE
PHONE : +66-2-576-1484~5
kec_th@kec.co.kr

CHINA
KEC SHANGHAI CO.,LTD.
PHONE : +86-21-5490-2277
kec_sh@kec.co.kr

KEC SHENZHEN OFFICE
PHONE : +86-755-8830-8899
kec_hk@kec.co.kr

KEC TIANJIN OFFICE
PHONE : +86-21-5490-2277
kec_sh@kec.co.kr

HONG KONG
KEC HK CORP. LTD
PHONE : +852-2249-3734
kec_hk@kec.co.kr

TAIWAN
KEC TAIWAN CO.,LTD.
PHONE : +886-2-2515-8359
kec_twn@kec.co.kr

U.S.A
KEC AMERICA CORP.
PHONE : +1-714-259-0662
kec_a@kec.co.kr

