

N-Ch Super Junction MOSFET

KP580N65FM3

This Super Junction MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge, low EMI characteristics and excellent avalanche characteristics. It is mainly suitable for power factor correction and switching mode power supplies.

FEATURES

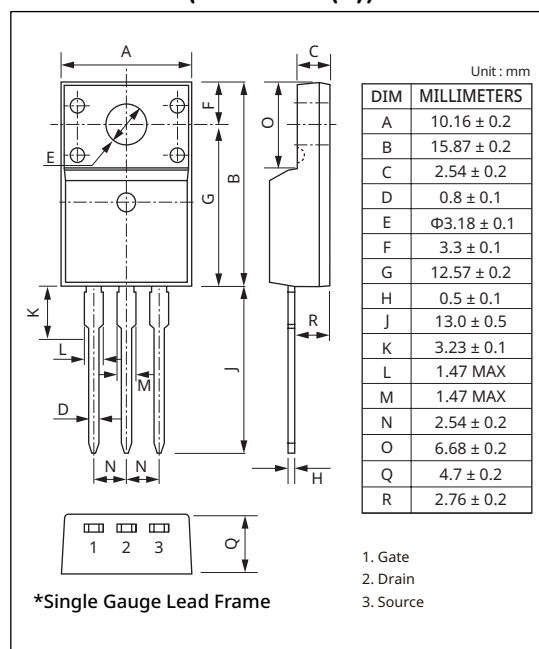
- $V_{DSS}=600V$, $I_D=8A$
- Drain-Source ON Resistance:
 $R_{DS(ON)}(MAX)=580m\Omega$ @ $V_{GS}=10V$
- $Q_g(typ.)=12nC$ @ $V_{GS}=10V$

APPLICATION

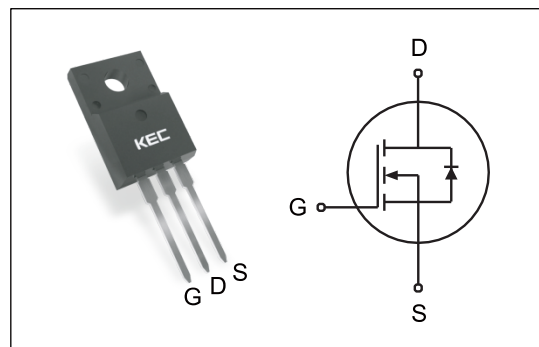
- PFC Stages
- resonant switching stages
- Fly Back converters
- Power of PC, TV, Lighting, Adapter, Server and Telecom

PACKAGE DIMENSION

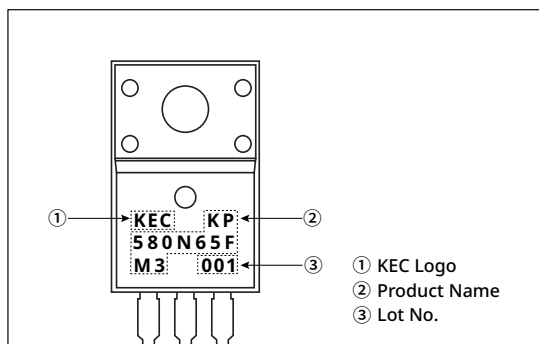
(TO-220IS(1))



PIN CONFIGURATION



MARKING CODE



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N-Ch Super Junction MOSFET - KP580N65FM3

MAXIMUM RATING (T_J=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V _{DSS}	650	V
Gate-Source Voltage		V _{GSS}	±30	V
Drain Current	@T _C =25°C	I _D	8*	A
	@T _C =100°C		5*	
	Pulsed (Note 1)	I _{DP}	22	
Single Pulsed Avalanche Energy (Note 2)		E _{AS}	170	mJ
Repetitive Avalanche Energy (Note 1)		E _{AR}	4.3	mJ
Peak MOSFET dv/dt		dv/dt	50	V/ns
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Drain Power Dissipation	T _C =25°C	P _D	43.1	W
	T _A =25°C		2	W
Maximum Junction Temperature		T _J	150	°C
Operation and Storage Temperature Range		T _{opr} , T _{stg}	-55 ~ 150	°C
Thermal Characteristics				
Thermal Resistance, Junction-to-Case		R _{thJC}	2.9	°C/W
Thermal Resistance, Junction-to-Ambient		R _{thJA}	62.5	°C/W

* : Drain current limited by maximum junction temperature

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ELECTRICAL CHARACTERISTICS (T_J=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	650	-	-	V
Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} /ΔT _J	I _D =1mA, Referenced to 25°C	-	0.7	-	V/°C
Drain Cut-off Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V	-	-	10	μA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Gate Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =4A	-	510	580	mΩ
Dynamic						
Total Gate Charge	Q _g	V _{DD} =400V, I _D =4A, V _{GS} =10V (Note 4)	-	12	-	nC
Gate-Source Charge	Q _{gs}		-	2.6	-	
Gate-Drain Charge	Q _{gd}		-	5	-	
Turn-on Delay time	t _{d(on)}	V _{DD} =400V, I _D =4A, V _{GS} =10V, R _G =25Ω (Note 4)	-	22	-	ns
Turn-on Rise time	t _r		-	19	-	
Turn-off Delay time	t _{d(off)}		-	65	-	
Turn-off Fall time	t _f		-	16	-	
Input Capacitance	C _{iss}	V _{DS} =400V, V _{GS} =0V, f=1.0MHz	-	505	-	pF
Output Capacitance	C _{oss}		-	14	-	
Reverse Transfer Capacitance	C _{rss}	V _{DS} =400V, V _{GS} =0V, f=250kHz	-	0.9	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} =0V, V _{DS} =0...400V (Note 5)	-	22	-	
Effective output capacitance, time related	C _{o(tr)}	V _{GS} =0V, V _{DS} =0...400V (Note 6)	-	110	-	
Gate Resistance	R _G	V _{GS} =0V, f=1.0MHz	-	19	-	Ω
Source-Drain Diode Ratings						
Continuous Source Current	I _S	V _{GS} < V _{th} , T _C =25°C	-	-	8	A
Pulsed Source Current	I _{SP}		-	-	24	
Diode Forward Voltage	V _{SD}	I _S =4A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	I _S =4A, V _{GS} =0V, V _{DD} =400V dI _S /dt=100A/μs	-	210	-	ns
Reverse Recovery Charge	Q _{rr}		-	1.8	-	μC

Note 1) Repetivity rating: Pulse width limited by junction temperature.

Note 2) L=80mH, I_D=2A, V_{DD}=50V, R_G=25Ω, Starting T_J=25°C.

Note 3) I_S ≤ I_D, di/dt ≤ 100A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J=25°C.

Note 4) Essentially independent of operating temperature.

Note 5) C_{o(er)} is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V.

Note 6) C_{o(tr)} is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 400V.

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Fig 1. $I_D - V_{DS}$ - 1

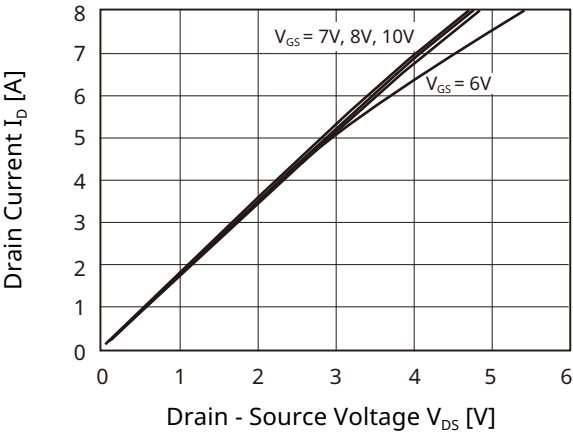


Fig 2. $I_D - V_{DS}$ - 2

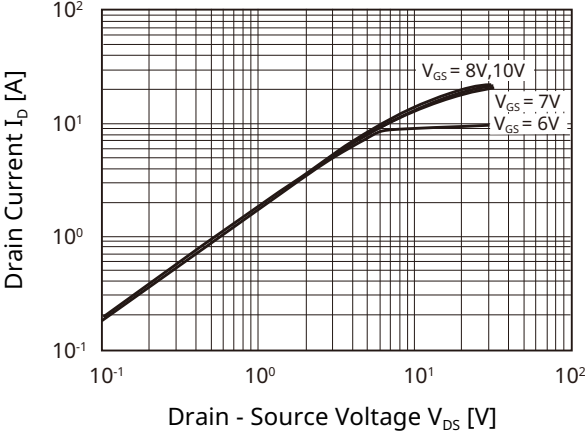


Fig 3. $I_D - V_{GS}$

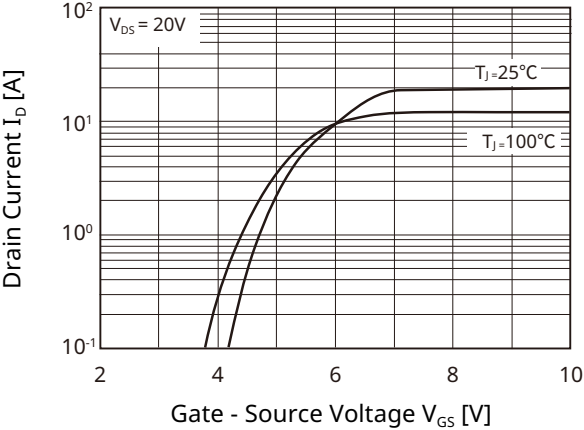


Fig 4. $R_{DS(ON)} - I_D$

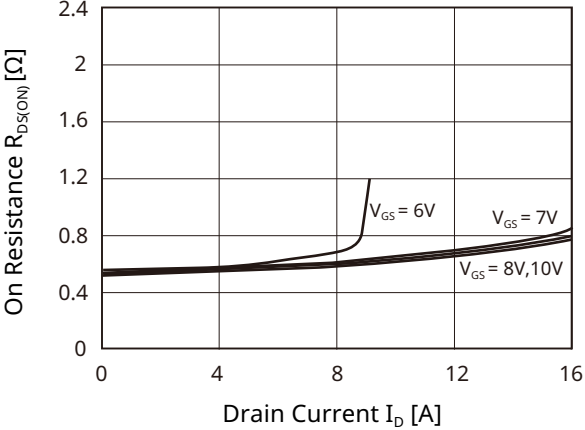


Fig 5. $R_{DS(ON)} - V_{GS}$

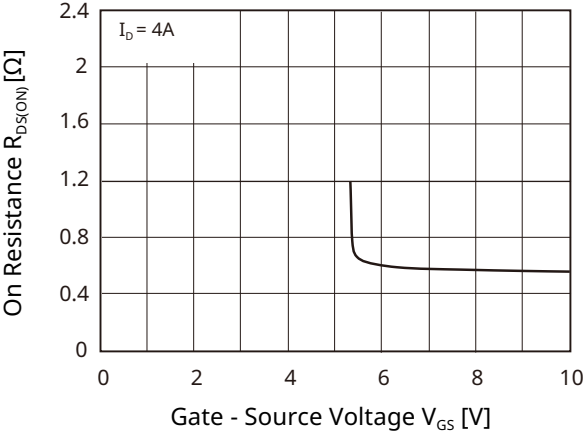
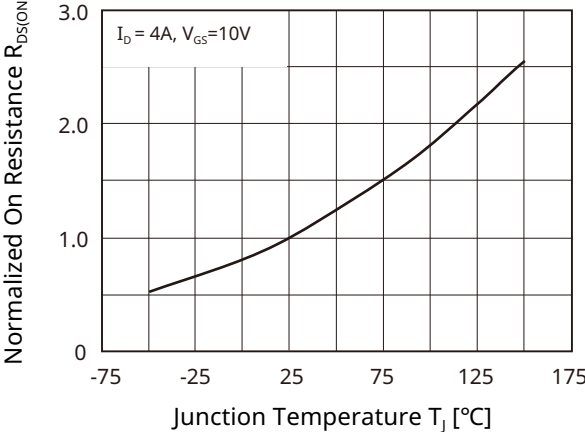


Fig 6. $R_{DS(ON)} - T_J$



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Fig 7. $BV_{DSS} - T_J$

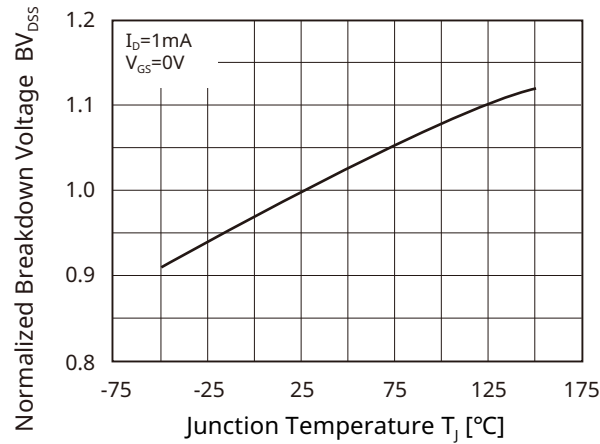


Fig 8. $V_{th} - T_J$

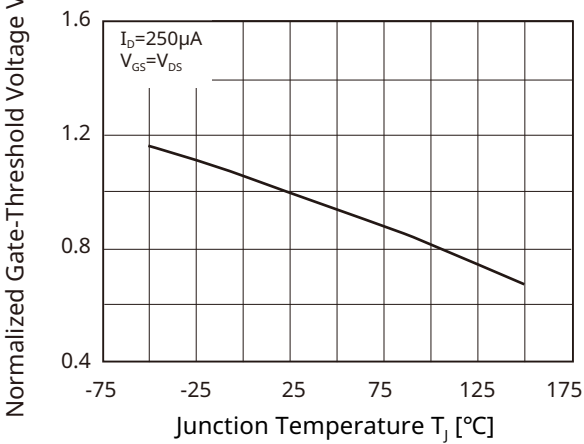


Fig 9. $I_S - V_{SD}$

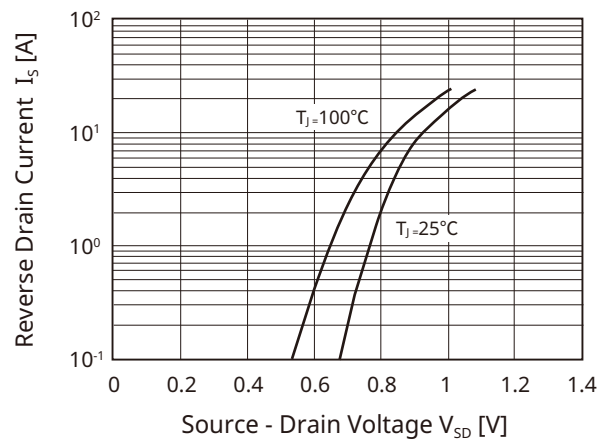


Fig 10. Capacitance V_{DS}

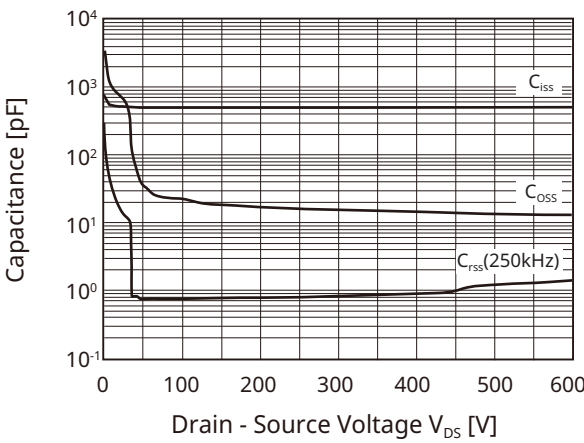


Fig 11. $E_{oss} - V_{DS}$

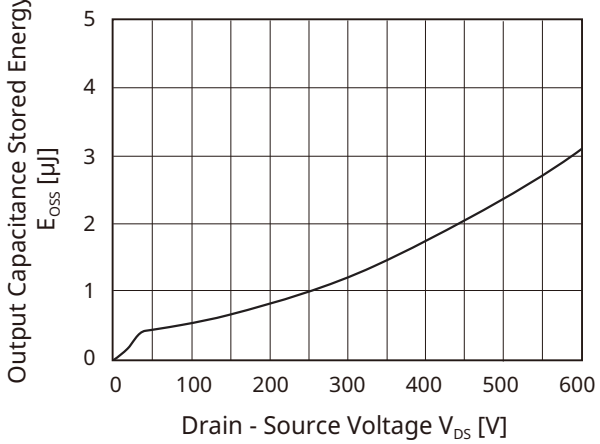
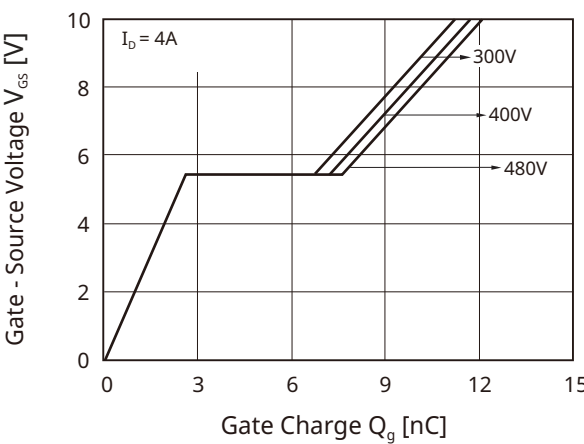


Fig 12. $Q_g - V_{GS}$



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Fig 13. Ptot - T_C

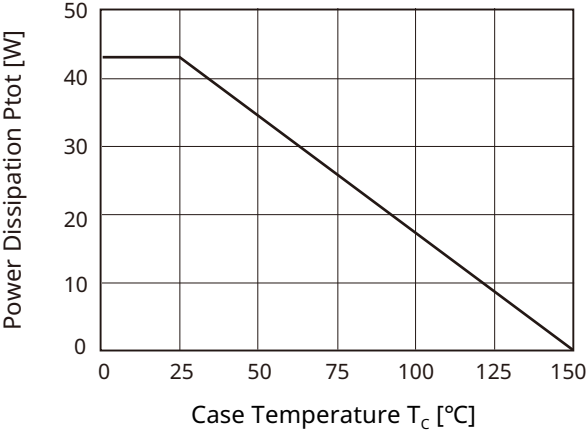


Fig 14. Switching Time - I_D

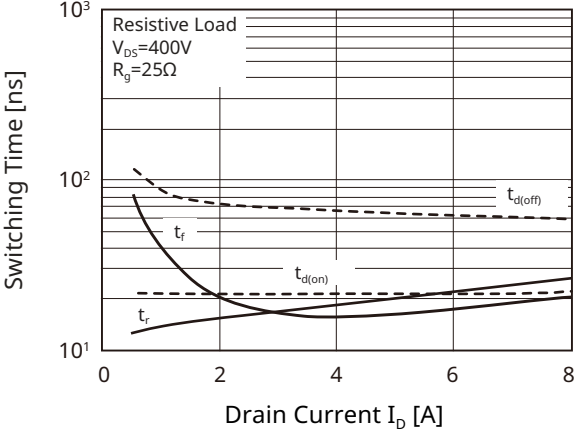


Fig 15. Switching Loss - I_D

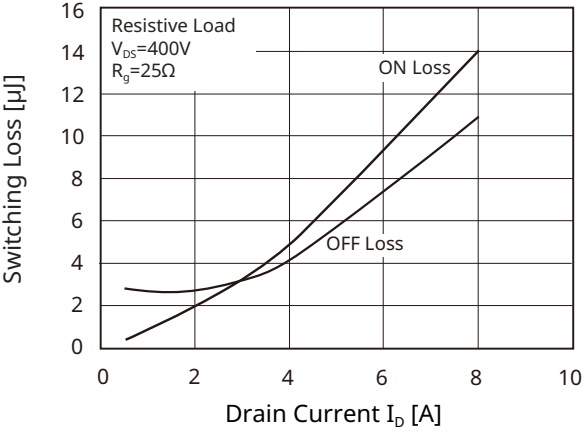


Fig 16. Switching Time - R_g

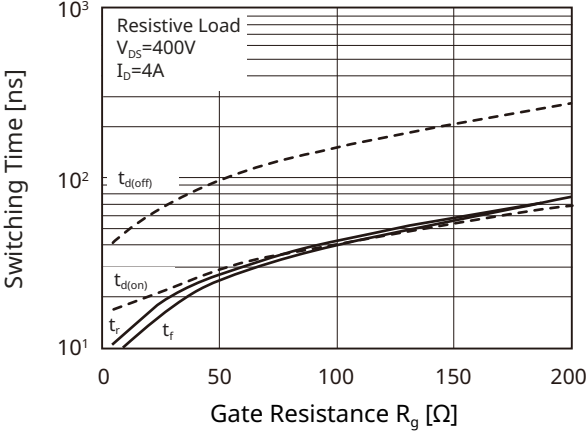


Fig 17. Switching Loss - R_g

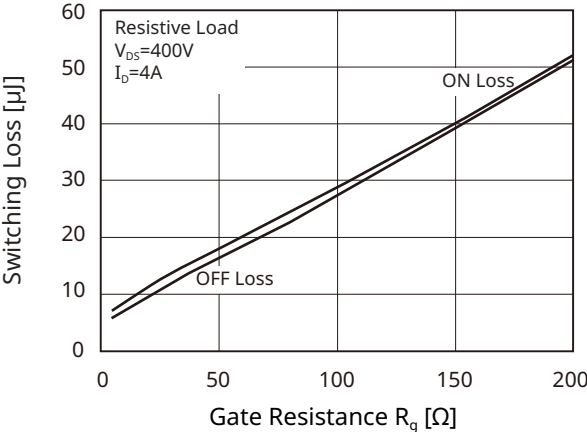
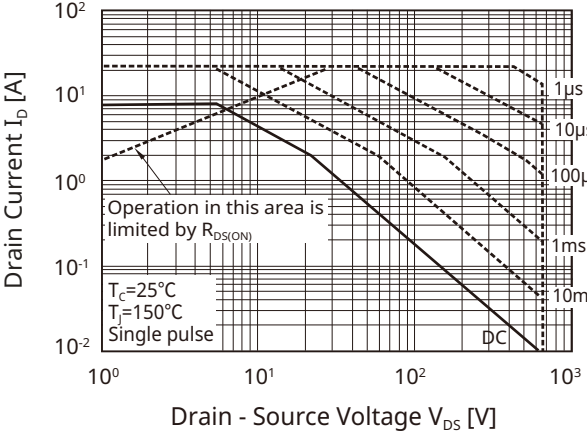


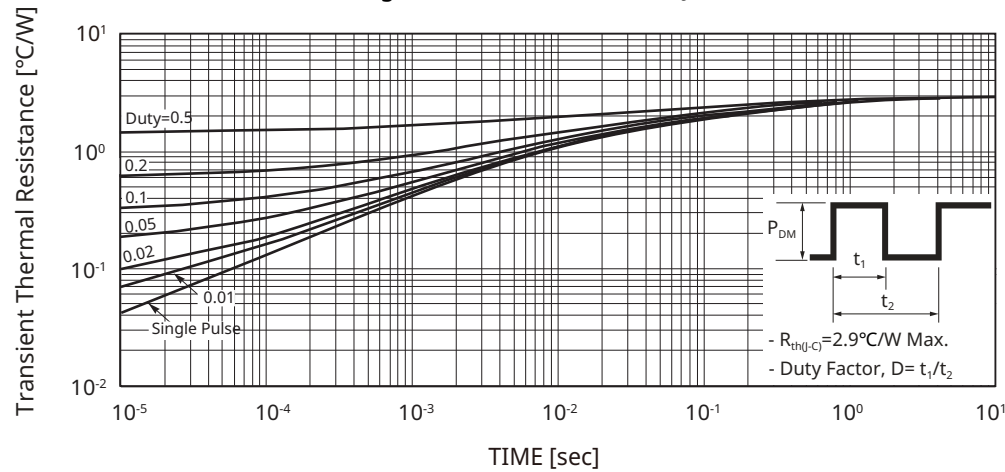
Fig 18. Safe Operation Area



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Fig 19. Transient Rth Curve (J-C)



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Fig 20. Gate Charge

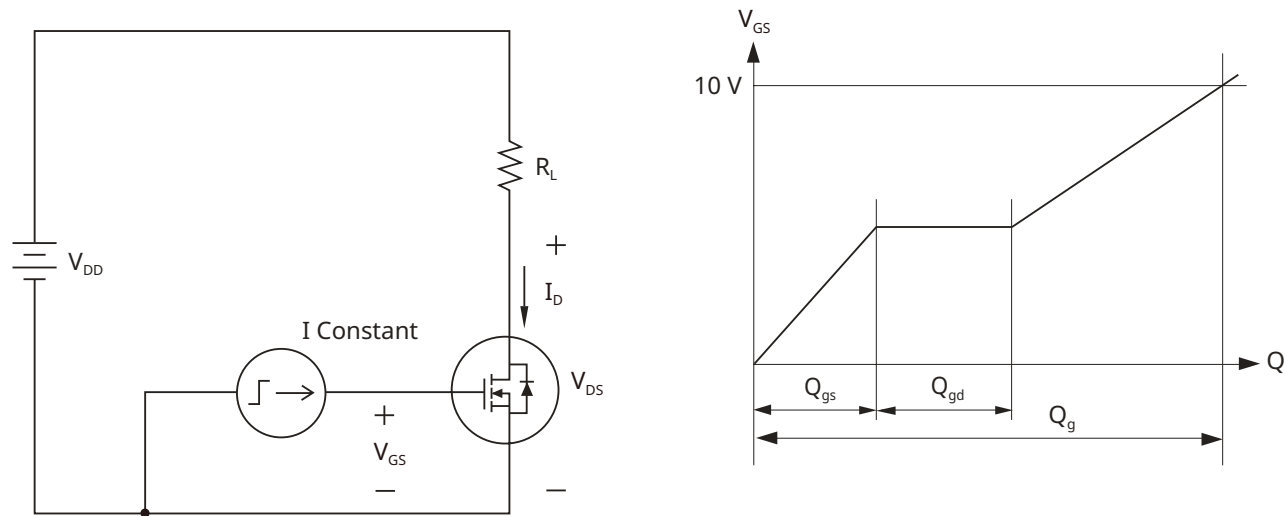


Fig 21. Single Pulsed Avalanche Energy

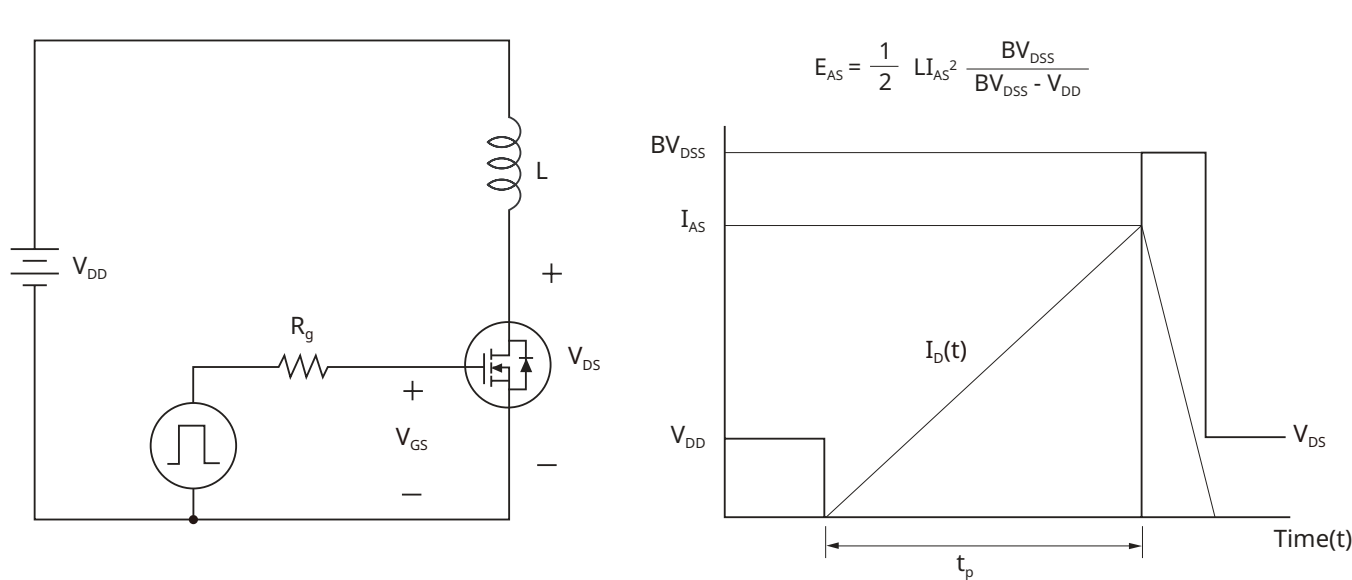
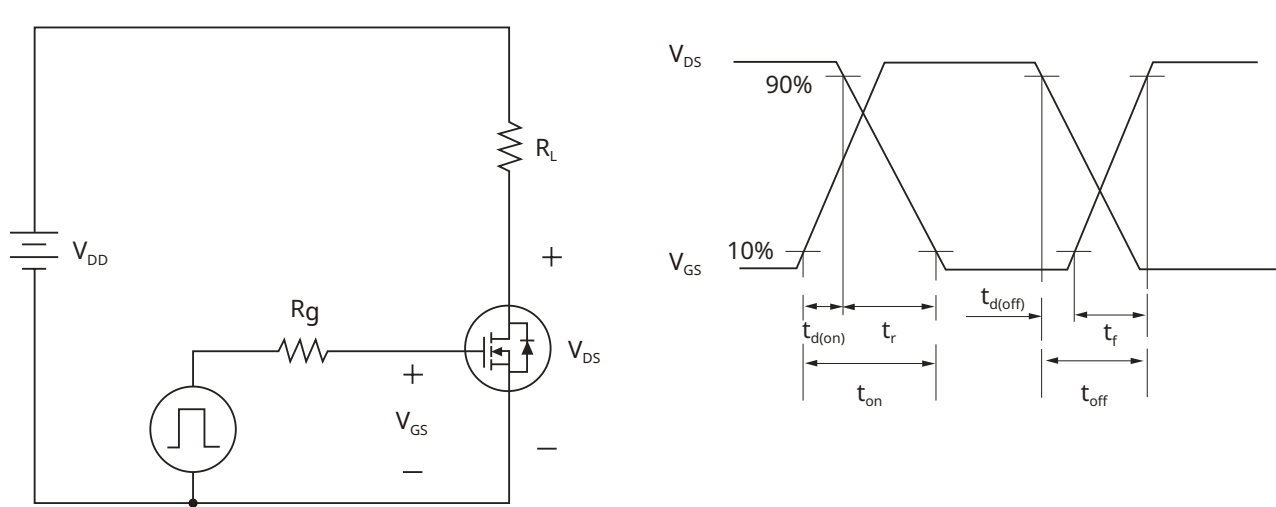


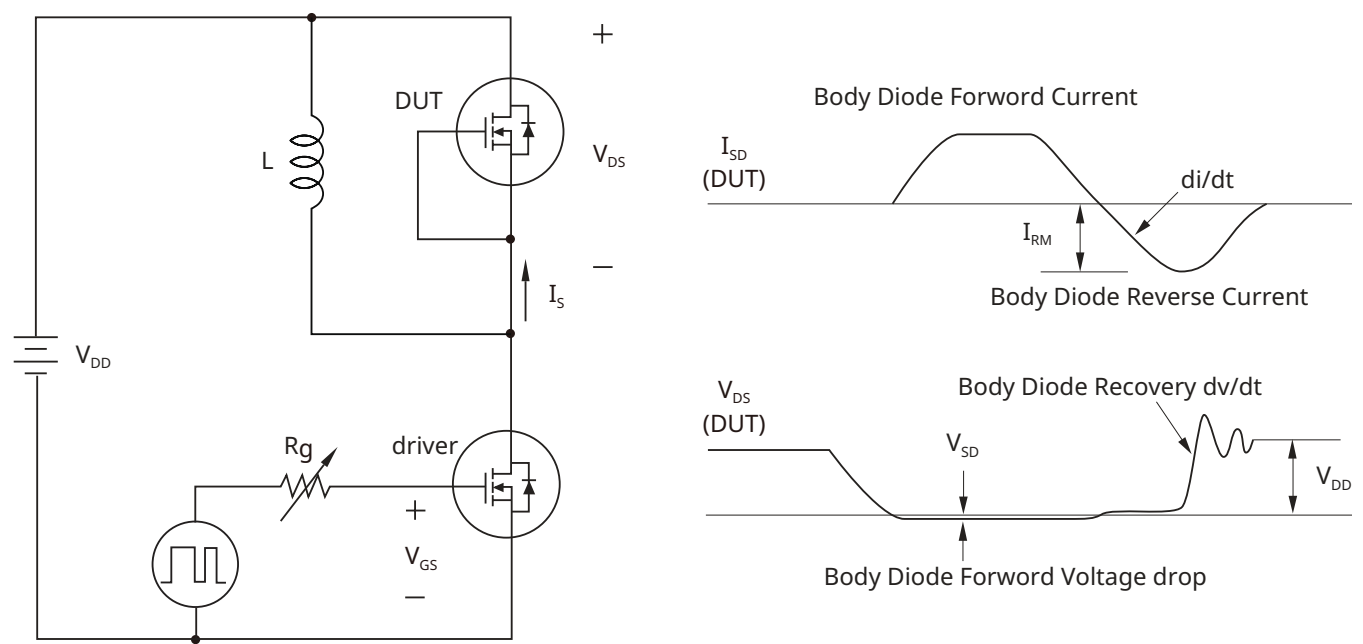
Fig 22. Resistive Load Switching



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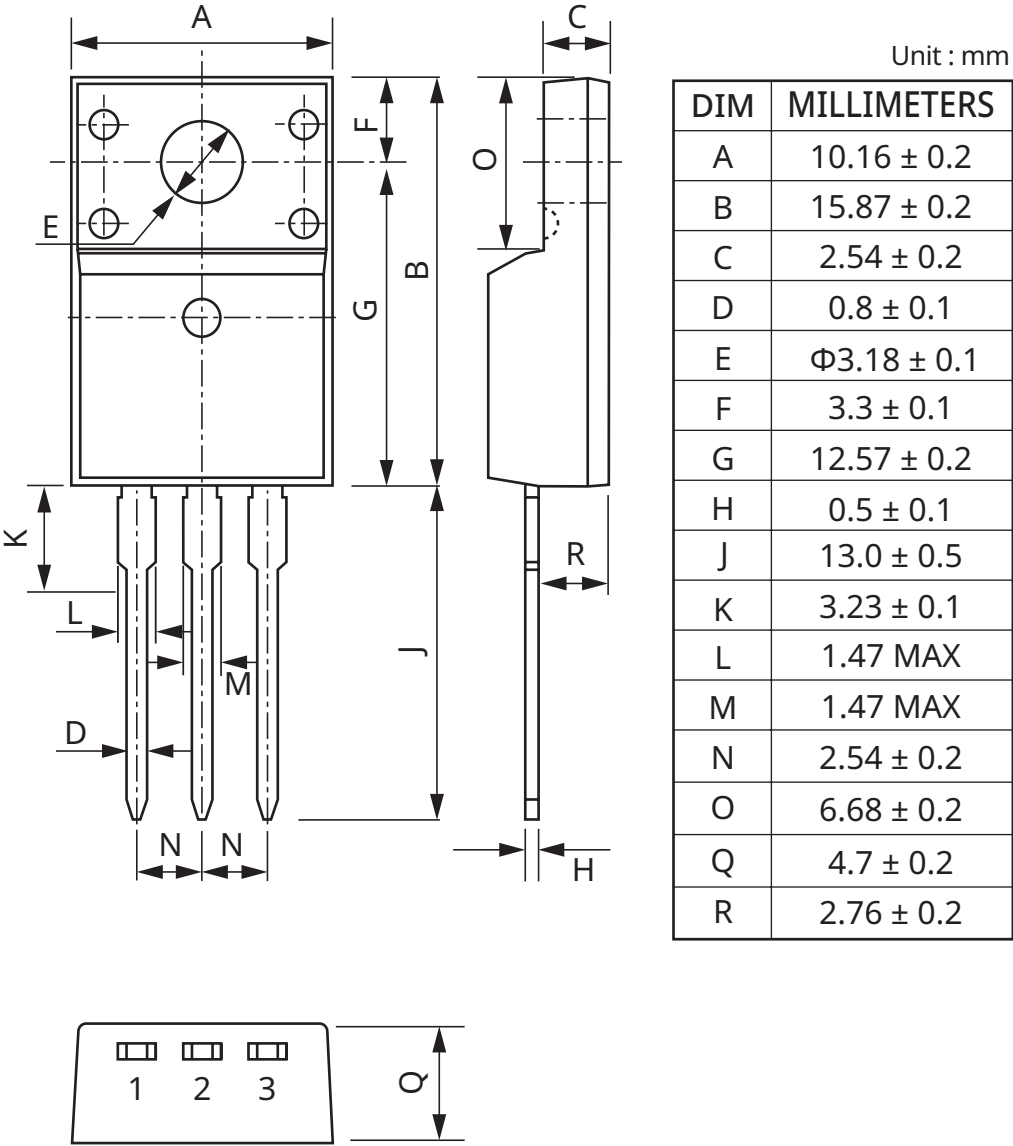
Fig 23. Source - Drain Diode Reverse Recovery and dv/dt



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PACKAGE INFORMATION(TO-220IS(1))



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ORDERING INFORMATION

Part Number	Status	Compliance		Packing Type	Reel Size	Quantity
		AEC-Q	Halogen Free			
KP580N65FM3-U/PF	Active	-	-	Tube	-	50

PRECAUTION ON USING KEC PRODUCTS

1. The products described in this data are intended to be used in general-purpose electronic equipment.
(Office equipment, telecommunication equipment, measuring equipment, home appliances)
2. When you intend to use these products with equipment or device which require an extremely high of reliability and special applications (such as automobile, air travel aerospace, transportation equipment, life support, system and safety devices) in which special quality and reliability and the failure or malfunction of products may directly jeopardize or harm the human body or damage to property and any application other than the standard application intended, please be sure to consult with our sales representative in advance.
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5. What are described in the data may be changed without any prior notice to reflect new technical development. Please confirm that you have received the latest product standards or specification before final design, purchase or use.
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