

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction and switching mode power supplies.

FEATURES

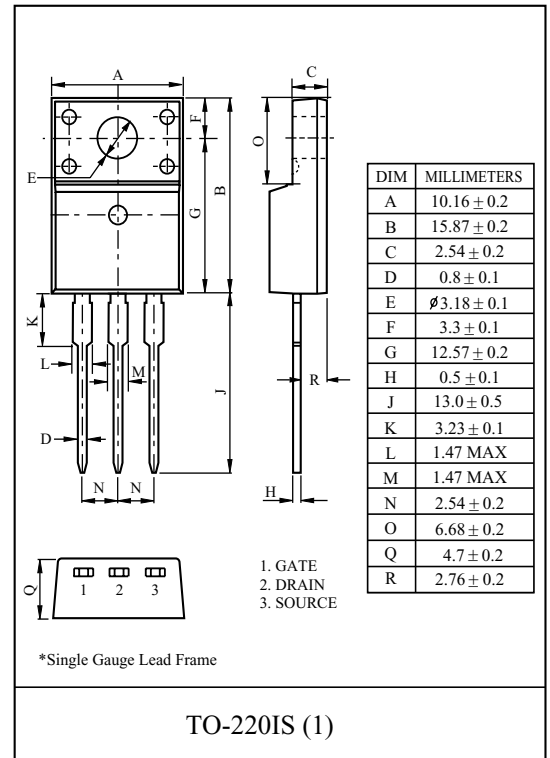
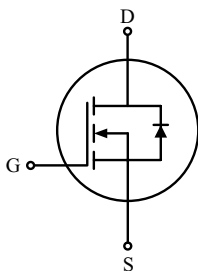
- $V_{DSS}=700V$, $I_D=6A$
- Drain-Source ON Resistance :
 $R_{DS(ON)}(Max)=1.65$ @ $V_{GS}=10V$
- $Qg(typ.)= 19nC$

MAXIMUM RATING (Tc=25)

| CHARACTERISTIC | | SYMBOL | RATING | UNIT |
|---|-----------------|------------|----------|------|
| Drain-Source Voltage | | V_{DSS} | 700 | V |
| Gate-Source Voltage | | V_{GSS} | ± 30 | V |
| Drain Current | @Tc=25 | I_D | 6* | A |
| | @Tc=100 | | 4* | |
| | Pulsed (Note1) | I_{DP} | 15* | |
| Single Pulsed Avalanche Energy (Note 2) | | E_{AS} | 160 | mJ |
| Repetitive Avalanche Energy (Note 1) | | E_{AR} | 4.2 | mJ |
| Peak Diode Recovery dv/dt (Note 3) | | dv/dt | 4.5 | V/ns |
| Drain Power Dissipation | Tc=25 | P_D | 41 | W |
| | Derate above 25 | | 0.33 | W/ |
| Maximum Junction Temperature | | T_j | 150 | |
| Storage Temperature Range | | T_{stg} | -55 150 | |
| Thermal Characteristics | | | | |
| Thermal Resistance, Junction-to-Case | | R_{thJC} | 3.05 | /W |
| Thermal Resistance, Junction-to-Ambient | | R_{thJA} | 62.5 | /W |

* : Drain current limited by maximum junction temperature.

PIN CONNECTION



KF6N70F

ELECTRICAL CHARACTERISTICS (Tc=25)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---|----------------|---|------|------|-----------|---------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $I_D=250\ \mu A, V_{GS}=0V$ | 700 | - | - | V |
| Breakdown Voltage Temperature Coefficient | BV_{DSS}/T_j | $I_D=250\ \mu A$, Referenced to 25 | - | 0.65 | - | V/ |
| Drain Cut-off Current | I_{DSS} | $V_{DS}=700V, V_{GS}=0V$ | - | - | 10 | μA |
| Gate Threshold Voltage | V_{th} | $V_{DS}=V_{GS}, I_D=250\ \mu A$ | 2.5 | - | 4.5 | V |
| Gate Leakage Current | I_{GSS} | $V_{GS}=\pm 30V, V_{DS}=0V$ | - | - | ± 100 | nA |
| Drain-Source ON Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=3A$ | - | 1.42 | 1.65 | |
| Dynamic | | | | | | |
| Total Gate Charge | Q_g | $V_{DS}=560V, I_D=6A$ $V_{GS}=10V$ (Note4,5) | - | 19 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 4 | - | |
| Gate-Drain Charge | Q_{gd} | | - | 8 | - | |
| Turn-on Delay time | $t_{d(on)}$ | $V_{DD}=350V$ $I_D=6A$ $R_G=25$ (Note4,5) | - | 34 | - | ns |
| Turn-on Rise time | t_r | | - | 32 | - | |
| Turn-off Delay time | $t_{d(off)}$ | | - | 90 | - | |
| Turn-off Fall time | t_f | | - | 33 | - | |
| Input Capacitance | C_{iss} | $V_{DS}=25V, V_{GS}=0V, f=1.0MHz$ | - | 785 | - | pF |
| Output Capacitance | C_{oss} | | - | 91 | - | |
| Reverse Transfer Capacitance | C_{rss} | | - | 9.1 | - | |
| Source-Drain Diode Ratings | | | | | | |
| Continuous Source Current | I_S | $V_{GS}<V_{th}$ | - | - | 6 | A |
| Pulsed Source Current | I_{SP} | | - | - | 24 | |
| Diode Forward Voltage | V_{SD} | $I_S=6.0A, V_{GS}=0V$ | - | - | 1.4 | V |
| Reverse Recovery Time | t_{rr} | $I_S=6.0A, V_{GS}=0V,$ $dI_S/dt=100A/\mu s$ | - | 345 | - | ns |
| Reverse Recovery Charge | Q_{rr} | | - | 3 | - | μC |

Note 1) Repetivity rating : Pulse width limited by junction temperature.

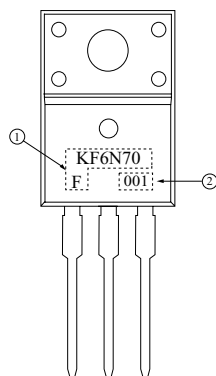
Note 2) $L=8.4mH, I_S=6A, V_{DD}=50V, R_G=25$, Starting $T_j=25$.

Note 3) $I_S=6A, dI/dt=100A/\mu s, V_{DD}=BV_{DSS}$, Starting $T_j=25$.

Note 4) Pulse Test : Pulse width $300\mu s$, Duty Cycle 2%.

Note 5) Essentially independent of operating temperature.

Marking



- ① PRODUCT NAME
- ② LOT NO

Fig 1. $I_D - V_{DS}$

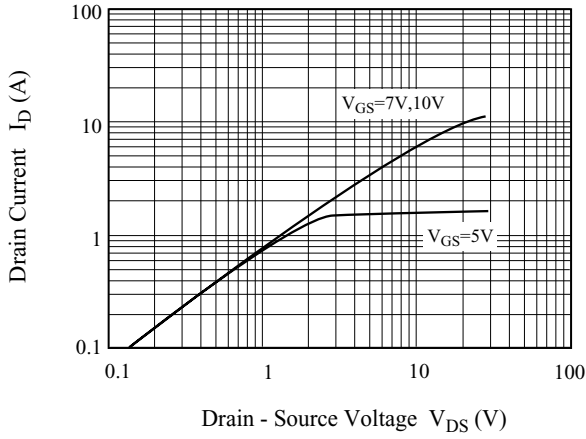


Fig 2. $I_D - V_{GS}$

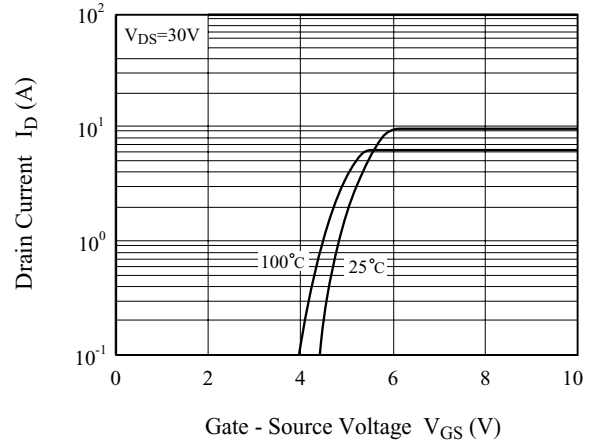


Fig 3. $BV_{DSS} - T_j$

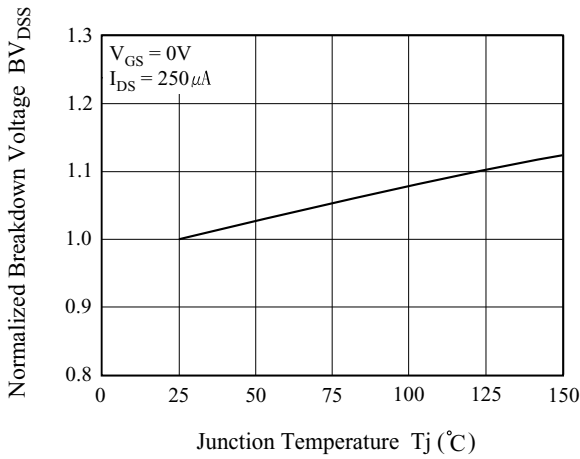


Fig 4. $R_{DS(ON)} - I_D$

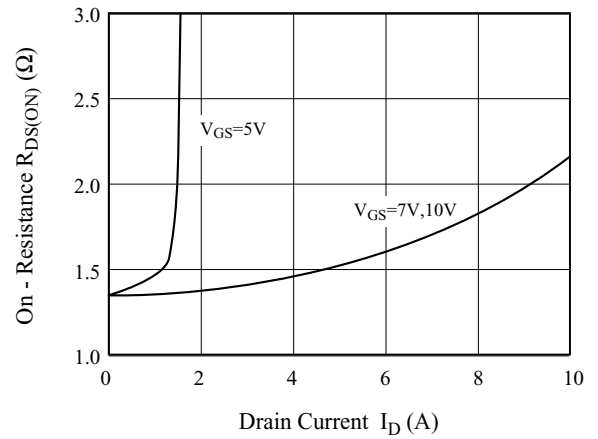


Fig 5. $I_S - V_{SD}$

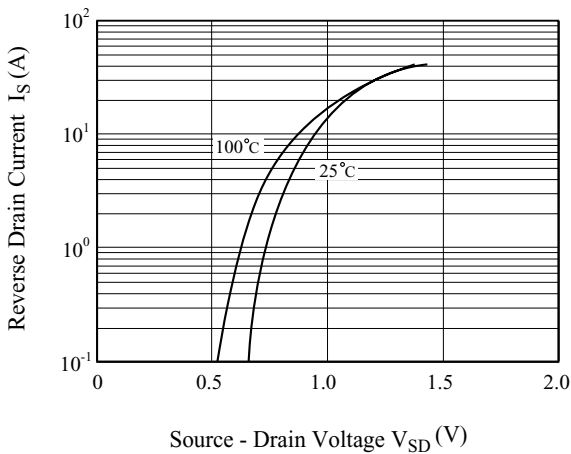
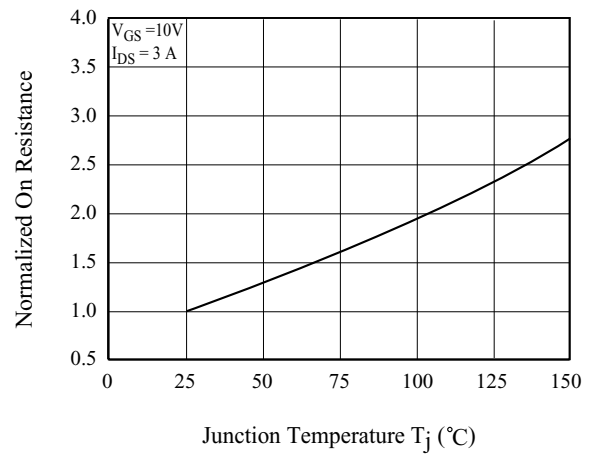


Fig 6. $R_{DS(ON)} - T_j$



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Fig 7. C - V_{DS}

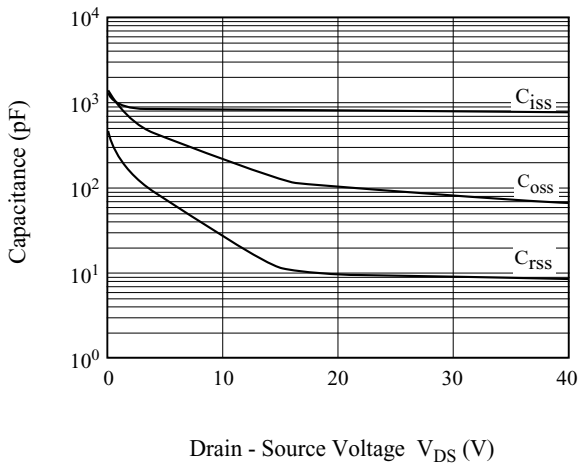


Fig 8. Q_g- V_{GS}

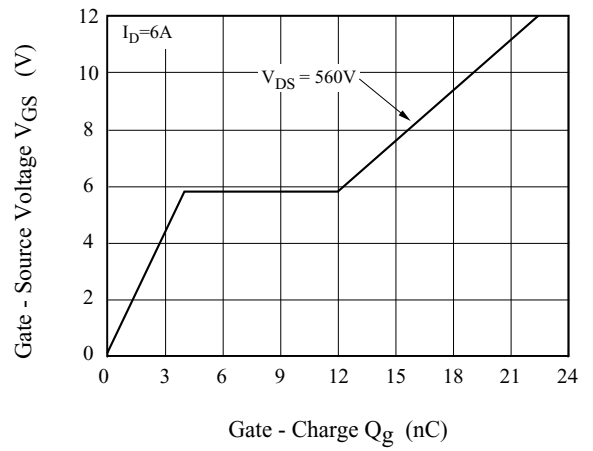


Fig 9. Safe Operation Area

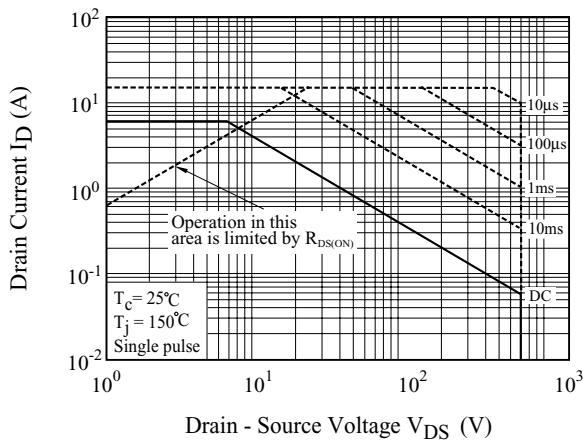


Fig 10. Transient Thermal Response Curve

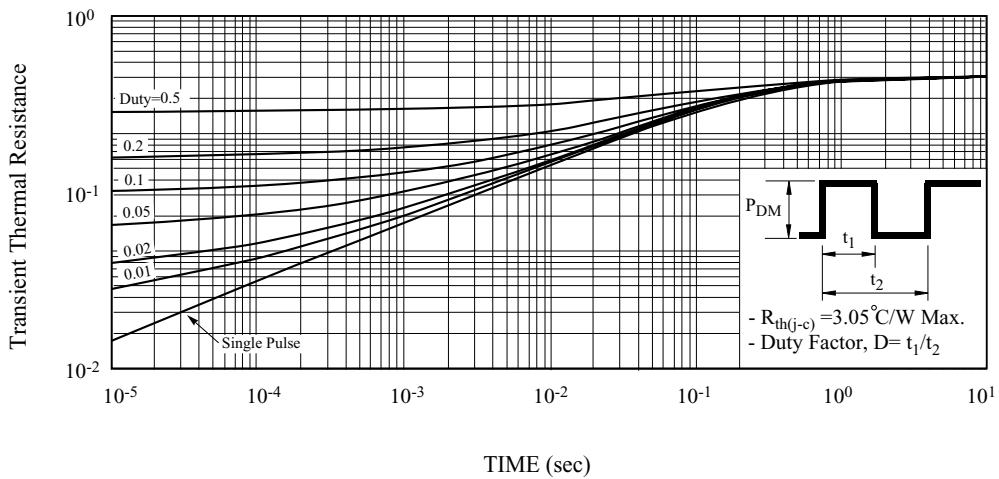


Fig 11. Gate Charge

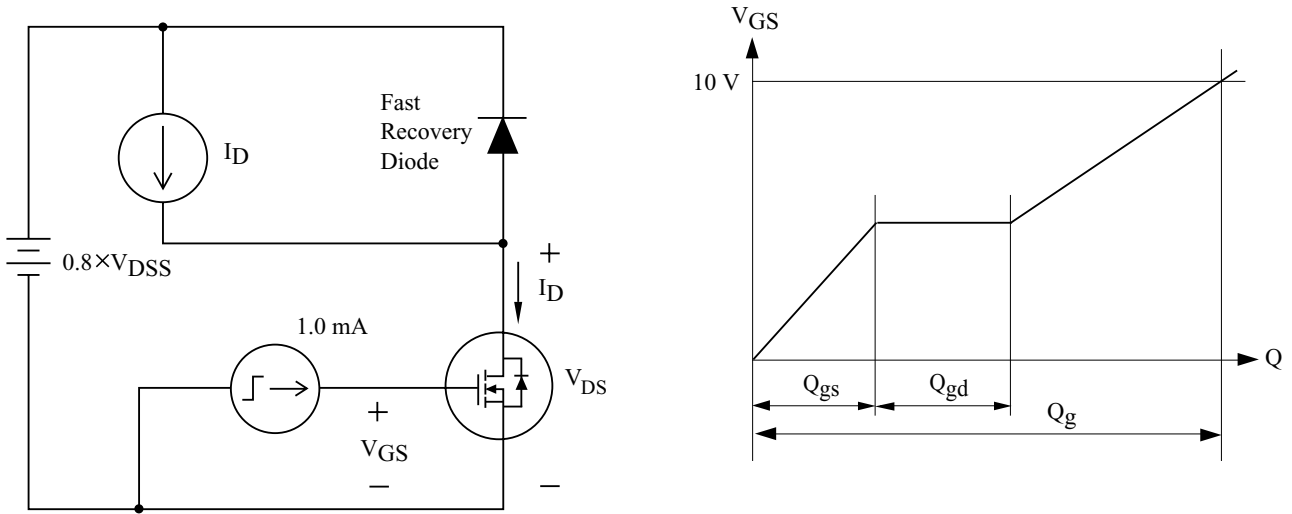


Fig 12. Single Pulsed Avalanche Energy

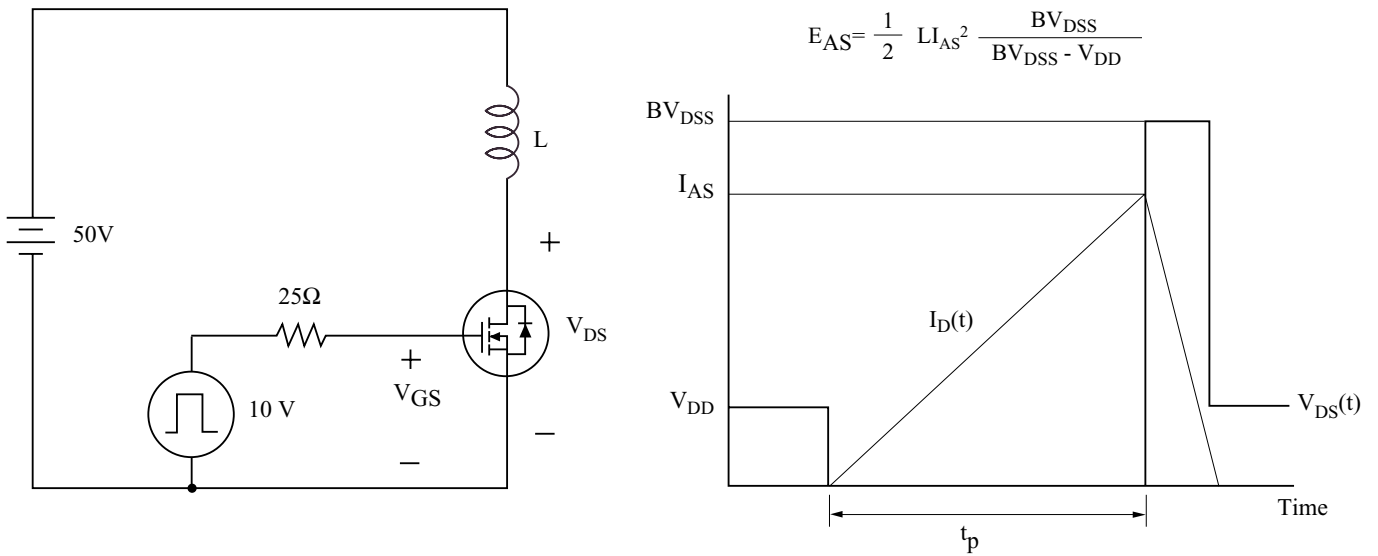


Fig 13. Resistive Load Switching

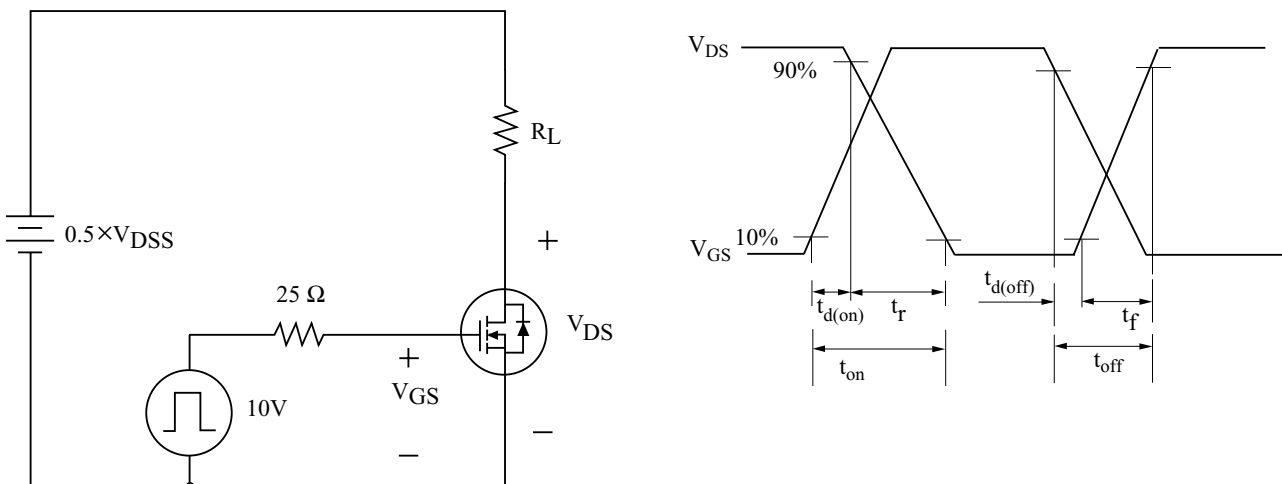


Fig 14. Source - Drain Diode Reverse Recovery and dv/dt

